

#### Features

- 1GHz -3dB bandwidth
- 9mA supply current
- Single and dual supply operation, from 5V to 10V supply span
- Available in 5-pin SOT23 package
- High speed, 600MHz product available (EL5192C, EL5292C, and EL5392C)
- Lower power, 300MHz product available (EL5193C, EL5293C, EL5393C)

#### Applications

- Video Amplifiers
- Cable Drivers
- RGB Amplifiers
- Test Equipment
- Instrumentation
- Current to Voltage Converters

#### **Ordering Information**

Part No	Package	Tape & Reel	Outline #
EL5191CW-T7	5-Pin SOT23	7"	MDP0038
EL5191CW-T13	5-Pin SOT23	13"	MDP0038
EL5191CS	8-Pin SO	-	MDP0027
EL5191CS-T7	8-Pin SO	7"	MDP0027
EL5191CS-T13	8-Pin SO	13"	MDP0027

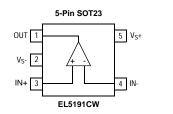
#### **General Description**

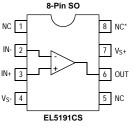
The EL5191C amplifier is of the current feedback variety and exhibits a very high bandwidth of 1GHz. This makes this amplifier ideal for today's high speed video and monitor applications, as well as a number of RF and IF frequency designs.

With a supply current of just 9mA and the ability to run from a single supply voltage from 5V to 10V, these amplifiers offer very high performance for little power consumption.

For applications where board space is critical, the EL5191C is offered in the 5-pin SOT23 package, as well as an industry standard 8-pin SO. The EL5191C operates over the industrial temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C.

#### **Pin Configurations**





\* This pin must be left disconnected

Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.

#### Absolute Maximum Ratings $(T_A = 25^{\circ}C)$

Values beyond absolute maximum ratings can cause the device to be pre-		
maturely damaged. Absolute maximum ratings are stress ratings only and	Power	
functional device operation is not implied.	Pin Vol	
Supply Voltage between V <sub>S</sub> + and V <sub>S</sub> -	V Storage	
Maximum Continuous Output Current 50mA		

Operating Junction Temperature Power Dissipation Pin Voltages Storage Temperature Operating Temperature  $\begin{array}{c} 125^{\circ}C\\ See\ Curves\\ V_{S^{-}} - 0.5V\ to\ V_{S^{+}} + 0.5V\\ -65^{\circ}C\ to\ +150^{\circ}C\\ -40^{\circ}C\ to\ +85^{\circ}C\end{array}$ 

#### Important Note:

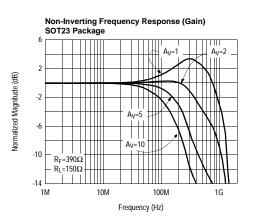
All parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore:  $T_J = T_C = T_A$ .

#### **Electrical Characteristics**

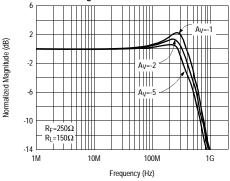
 $V_S+=+5V, V_S-=-5V, R_F=392\Omega \text{ for } A_V=1, R_F=250\Omega \text{ for } A_V=2, R_L=150\Omega, T_A=25^\circ C \text{ unless otherwise specified.}$ 

Parameter	Description	Conditions	Min	Тур	Max	Unit
AC Performa	nce		•		•	
BW -3dB Bandwidth		$A_{V} = +1$		1000		MHz
		$A_{V} = +2$		600		MHz
BW1	0.1dB Bandwidth			30		MHz
SR	Slew Rate	$V_0 = -2.5V$ to $+2.5V$ , $A_V = +2$	2500	2800		V/µs
ts	0.1% Settling Time	$V_{OUT} = -2.5V$ to $+2.5V$ , $A_V = -1$		7		ns
en	Input Voltage Noise			3.8		nV/√Hz
in-	IN- input current noise			25		pA/√Hz
in+	IN+ input current noise			55		pA/√Hz
dG	Differential Gain Error <sup>[1]</sup>	$A_{V} = +2$		0.035		%
dP	Differential Phase Error <sup>[1]</sup>	$A_{V} = +2$		0.04		0
DC Performa	nce		•	•		
V <sub>OS</sub>	Offset Voltage		-15	1	15	mV
T <sub>C</sub> V <sub>OS</sub>	Input Offset Voltage Temperature Coefficient	Measured from T <sub>MIN</sub> to T <sub>MAX</sub>		5		µV/°C
R <sub>OL</sub>	Transimpedance		150	300		kΩ
Input Charac	teristics		•	•		
CMIR	Common Mode Input Range		±3	±3.3		V
CMRR	Common Mode Rejection Ratio		42	50		dB
-ICMR	- Input Current Common Mode Rejection		-6		6	μA/V
$+I_{IN}$	+ Input Current		-120	40	120	μΑ
-I <sub>IN</sub>	- Input Current		-40	5	40	μΑ
R <sub>IN</sub>	Input Resistance			27		kΩ
CIN	Input Capacitance			0.5		pF
Output Chara	acteristics			•		
Vo	Output Voltage Swing	$R_L = 150\Omega$ to GND	±3.4	±3.7		V
		$R_L = 1K\Omega$ to GND	±3.8	±4.0		V
I <sub>OUT</sub>	Output Current	$R_L = 10\Omega$ to GND	95	120		mA
Supply						
Ison	Supply Current	No Load, $V_{IN} = 0V$	8	9	10.5	mA
PSRR	Power Supply Rejection Ratio	DC, $V_S = \pm 4.75V$ to $\pm 5.25V$	55	75		dB
-IPSR	- Input Current Power Supply Rejection	DC, $V_S = \pm 4.75V$ to $\pm 5.25V$	-2		2	μA/V

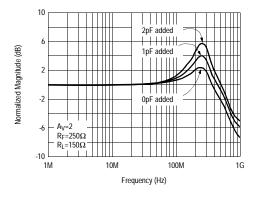
1. Standard NTSC test, AC signal amplitude =  $286mV_{P-P}$ , f = 3.58MHz



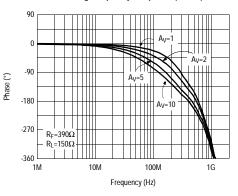




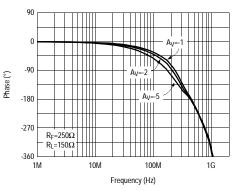
Frequency Response for Various CIN-



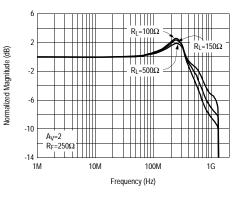
Non-Inverting Frequency Response (Phase)



Inverting Frequency Response (Phase)



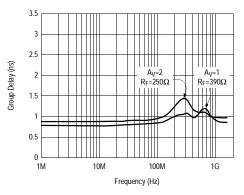
Frequency Response for Various RL



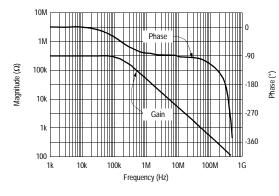
#### **Typical Performance Curves**

Frequency Response for Various CL 14 10 Normalized Magnitude (dB) 6pF added 1111 6 4pF added 1111 2 Ay=2 -2 OpF added ф, R<sub>F</sub>=250Ω 111 R<sub>L</sub>=150Ω -6 10M 1M 100M 1G Frequency (Hz)

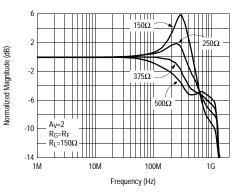
Group Delay vs Frequency



Transimpedance (ROL) vs Frequency



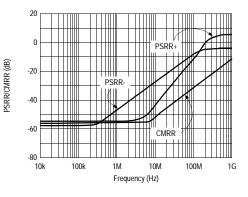
Frequency Response for Various R<sub>F</sub>



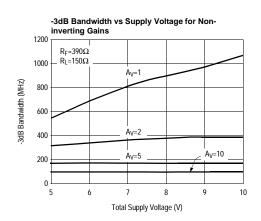
Input Voltages 6 тппп ПШ V<sub>CM</sub>=0V V<sub>CM</sub>=3V ĨĨIħ ТИ 2 -2 V<sub>CM</sub>=-3V -6 A<sub>V</sub>=2 -10 R<sub>F</sub>=250Ω  $R_L=150\Omega$ 1 -14 10M 100M 1G 1M Frequency (Hz)

Frequency Response for Various Common-mode

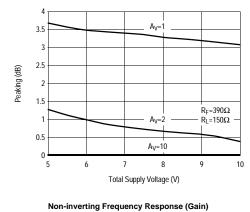
PSRR and CMRR vs Frequency

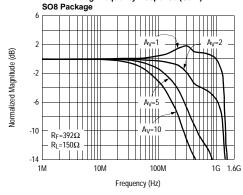


Normalized Magnitude (dB)



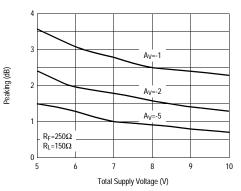
Peaking vs Supply Voltage for Non-inverting Gains



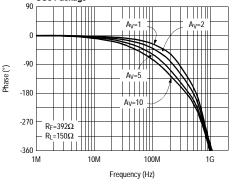


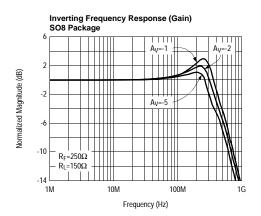
-3dB Bandwidth vs Supply Voltage for Inverting Gains 600 500 Av .2 3dB Bandwidth (MHz) 400 300 Av -5 200 100 R<sub>F</sub>=250Ω R<sub>L</sub>=150Ω 0 7 8 9 5 6 10 Total Supply Voltage (V)

Peaking vs Supply Voltage for Inverting Gains

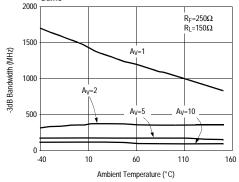


Non-inverting Frequency Response (Phase) SO8 Package

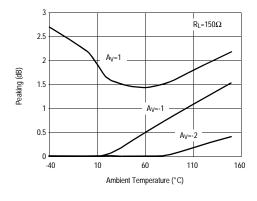




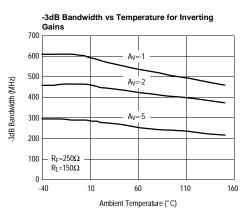
-3dB Bandwidth vs Temperature for Non-inverting Gains



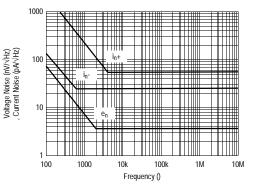




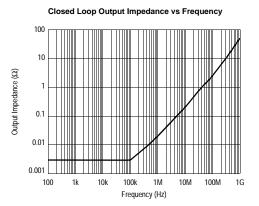
Inverting Frequency Response (Phase) SO8 Package 90 Av=-1 Av 0 -90 Phase (°) Av=-5 -180 -270 R<sub>F</sub>=250Ω RL=150Ω -360 1M 10M 100M 1G Frequency (Hz)



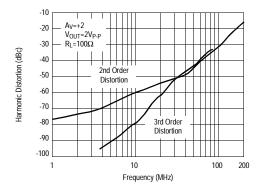
Voltage and Current Noise vs Frequency

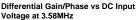


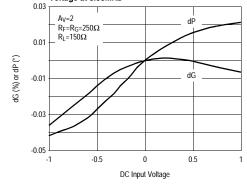
#### **Typical Performance Curves**



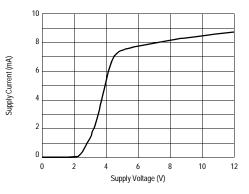
#### 2nd and 3rd Harmonic Distortion vs Frequency

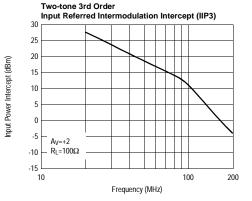




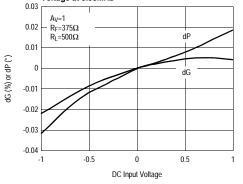


Supply Current vs Supply Voltage

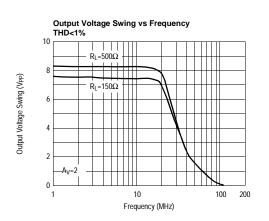




Differential Gain/Phase vs DC Input Voltage at 3.58MHz

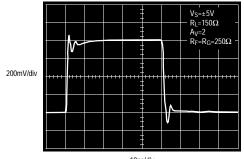


#### **Typical Performance Curves**

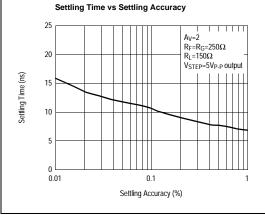


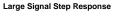
Output Voltage Swing vs Frequency THD-0.1%  $R_L=500\Omega$  $R_L=150\Omega$  $A_{V=2}$  $A_{V=2}$ 1010 $R_{L}=10$ 10 $R_{L}=100$ 10 $R_{L}=100$ 10 $R_{L}=100$ 10 $R_{L}=100$  $R_{L}=100$ 

Small Signal Step Response

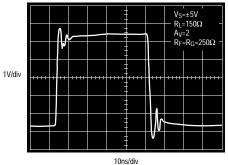


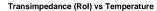


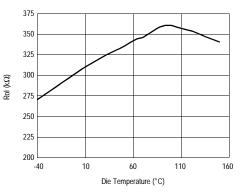


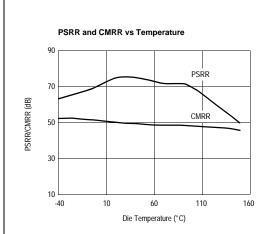


Output Voltage Swing (VPP)

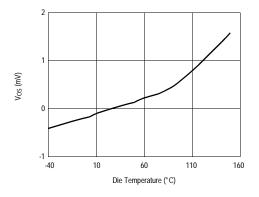




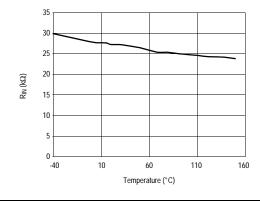




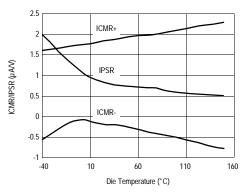
Offset Voltage vs Temperature



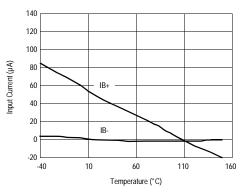
Positive Input Resistance vs Temperature



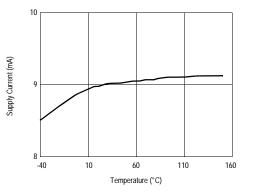
ICMR and IPSR vs Temperature

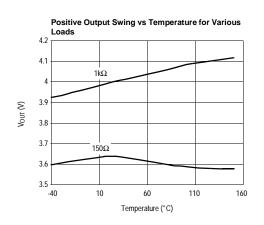


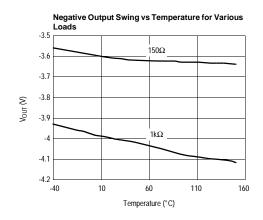
Input Current vs Temperature



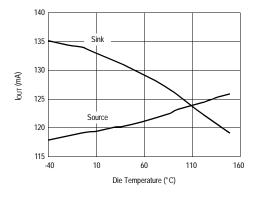
Supply Current vs Temperature



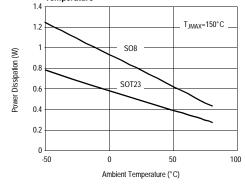




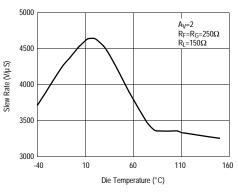
**Output Current vs Temperature** 



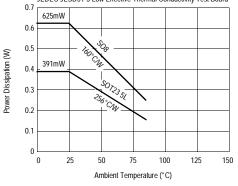
Maximum Power Dissipation vs Ambient Temperature







Package Power Dissipation vs Ambient Temp. JEDEC JESD51-3 Low Effective Thermal Conductivity Test Board



Pin Descriptions					
EL5191C	EL5191C				
8-Pin SO	5-Pin SOT23	Pin Name	Function	Equivalent Circuit	
1,5		NC	Not connected		
2	4	IN-	Inverting input		
3	3	IN+	Non-inverting input	(See circuit 1)	
4	2	V <sub>S</sub> -	Negative supply		
6	1	OUT	Output		
7	5	$V_{S}$ +	Positive supply		
8		NC	Not connected (leave this pin disconnected)		

#### **Applications Information**

#### **Product Description**

The EL5191C is a current-feedback operational amplifier that offers a wide -3dB bandwidth of 1GHz and a low supply current of 9mA per amplifier. The EL5191C works with supply voltages ranging from a single 5V to 10V and they are also capable of swinging to within 1V of either supply on the output. Because of their currentfeedback topology, the EL5191C does not have the normal gain-bandwidth product associated with voltagefeedback operational amplifiers. Instead, its -3dB bandwidth to remain relatively constant as closed-loop gain is increased. This combination of high bandwidth and low power, together with aggressive pricing make the EL5191C the ideal choice for many low-power/highbandwidth applications such as portable, handheld, or battery-powered equipment.

For varying bandwidth needs, consider the EL5192C with 600MHz on a 6mA supply current or the EL5193C with 300MHz on a 4mA supply current. Versions include single, dual, and triple amp packages with 5-pin SOT23, 16-pin QSOP, and 8-pin or 16-pin SO outlines.

# Power Supply Bypassing and Printed Circuit Board Layout

As with any high frequency device, good printed circuit board layout is necessary for optimum performance. Low impedance ground plane construction is essential. Surface mount components are recommended, but if leaded components are used, lead lengths should be as short as possible. The power supply pins must be well bypassed to reduce the risk of oscillation. The combination of a  $4.7\mu$ F tantalum capacitor in parallel with a  $0.01\mu$ F capacitor has been shown to work well when placed at each supply pin.

For good AC performance, parasitic capacitance should be kept to a minimum, especially at the inverting input. (See the Capacitance at the Inverting Input section) Even when ground plane construction is used, it should be removed from the area near the inverting input to minimize any stray capacitance at that node. Carbon or Metal-Film resistors are acceptable with the Metal-Film resistors giving slightly less peaking and bandwidth because of additional series inductance. Use of sockets, particularly for the SO package, should be avoided if possible. Sockets add parasitic inductance and capacitance which will result in additional peaking and overshoot.

#### **Capacitance at the Inverting Input**

Any manufacturer's high-speed voltage- or currentfeedback amplifier can be affected by stray capacitance at the inverting input. For inverting gains, this parasitic capacitance has little effect because the inverting input is a virtual ground. But for non-inverting gains, this capacitance (in conjunction with the feedback and gain resistors) creates a pole in the feedback path of the amplifier. This pole, if low enough in frequency, has the same destabilizing effect as a zero in the forward openloop response. The use of large value feedback and gain resistors exacerbates the problem by further lowering the pole frequency (increasing the possibility of oscillation.)

The EL5191C has been optimized with a  $250\Omega$  feedback resistor. With the high bandwidth of these amplifiers, these resistor values might cause stability problems when combined with parasitic capacitance, thus ground plane is not recommended around the inverting input pin of the amplifier.

#### Feedback Resistor Values

The EL5191C has been designed and specified at a gain of +2 with  $R_F$  approximately 250 $\Omega$ . This value of feedback resistor gives 600MHz of -3dB bandwidth at  $A_V\!=\!2$  with about 2dB of peaking. With  $A_V\!=\!-2$ , that same  $R_F$  gives 450MHz of bandwidth with 0.6dB of peaking. Since the EL5191C is a current-feedback amplifier, it is also possible to change the value of  $R_F$  to get more bandwidth. As seen in the curve of Frequency Response for Various  $R_F$  and  $R_G$ , bandwidth and peaking can be easily modified by varying the value of the feedback resistor.

Because the EL5191C is a current-feedback amplifier, its gain-bandwidth product is not a constant for different closed-loop gains. This feature actually allows the EL5191C to maintain about the same -3dB bandwidth. As gain is increased, bandwidth decreases slightly while stability increases. Since the loop stability is improving with higher closed-loop gains, it becomes possible to reduce the value of  $R_F$  below the specified 250 $\Omega$  and still retain stability, resulting in only a slight loss of bandwidth with increased closed-loop gain.

# Supply Voltage Range and Single-Supply Operation

The EL5191C has been designed to operate with supply voltages having a span of greater than 5V and less than 10V. In practical terms, this means that the EL5191C will operate on dual supplies ranging from  $\pm 2.5V$  to  $\pm 5V$ . With single-supply, the EL5191C will operate from 5V to 10V.

As supply voltages continue to decrease, it becomes necessary to provide input and output voltage ranges that can get as close as possible to the supply voltages. The EL5191C has an input range which extends to within 2V of either supply. So, for example, on  $\pm$ 5V supplies, the EL5191C has an input range which spans  $\pm$ 3V. The output range of the EL5191C is also quite large, extending to within 1V of the supply rail. On a  $\pm$ 5V supply, the output is therefore capable of swinging from -4V to +4V. Single-supply output range is larger because of the increased negative swing due to the external pull-down resistor to ground.

#### **Video Performance**

For good video performance, an amplifier is required to maintain the same output impedance and the same frequency response as DC levels are changed at the output. This is especially difficult when driving a standard video load of  $150\Omega$ , because of the change in output current with DC level. Previously, good differential gain could only be achieved by running high idle currents through the output transistors (to reduce variations in output impedance.) These currents were typically comparable to the entire 9mA supply current of each EL5191C amplifier. Special circuitry has been incorporated in the EL5191C to reduce the variation of output impedance with current output. This results in dG and dP specifications of 0.035% and 0.04°, while driving 150 $\Omega$  at a gain of 2. Video performance has also been measured with a  $500\Omega$  load at a gain of +1. Under these conditions, the EL5191C has dG and dP specifications of 0.02% and 0.02°, respectively.

#### **Output Drive Capability**

In spite of its low 9mA of supply current, the EL5191C is capable of providing a minimum of  $\pm$ 95mA of output current. With a minimum of  $\pm$ 95mA of output drive, the EL5191C is capable of driving 50 $\Omega$  loads to both rails, making it an excellent choice for driving isolation transformers in telecommunications applications.

#### **Driving Cables and Capacitive Loads**

When used as a cable driver, double termination is always recommended for reflection-free performance. For those applications, the back-termination series resistor will decouple the EL5191C from the cable and allow extensive capacitive drive. However, other applications may have high capacitive loads without a back-termination resistor. In these applications, a small series resistor (usually between  $5\Omega$  and  $50\Omega$ ) can be placed in series with the output to eliminate most peaking. The gain resistor (R<sub>G</sub>) can then be chosen to make up for any gain loss which may be created by this additional resistor at the output. In many cases it is also possible to simply increase the value of the feedback resistor (R<sub>F</sub>) to reduce the peaking.

#### **Current Limiting**

The EL5191C has no internal current-limiting circuitry. If the output is shorted, it is possible to exceed the Absolute Maximum Rating for output current or power dissipation, potentially resulting in the destruction of the device.

#### **Power Dissipation**

With the high output drive capability of the EL5191C, it is possible to exceed the 125°C Absolute Maximum junction temperature under certain very high load current conditions. Generally speaking when  $R_L$  falls below about 25 $\Omega$ , it is important to calculate the maximum junction temperature ( $T_{JMAX}$ ) for the application to determine if power supply voltages, load conditions, or package type need to be modified for the EL5191C to

remain in the safe operating area. These parameters are calculated as follows:

 $T_{JMAX} = T_{MAX} + (\theta_{JA} \times n \times PD_{MAX})$ 

where:

T<sub>MAX</sub> = Maximum Ambient Temperature

 $\theta_{IA}$  = Thermal Resistance of the Package

n = Number of Amplifiers in the Package

 $PD_{MAX}$  = Maximum Power Dissipation of Each Amplifier in the Package

 $\ensuremath{\text{PD}_{\text{MAX}}}$  for each amplifier can be calculated as follows:

$$PD_{MAX} = (2 \times V_S \times I_{SMAX}) + \left[ (V_S - V_{OUTMAX}) \times \frac{V_{OUTMAX}}{R_L} \right]$$

where:

 $V_S =$  Supply Voltage

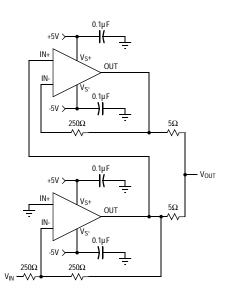
 $I_{SMAX}$  = Maximum Supply Current of 1A

V<sub>OUTMAX</sub> = Maximum Output Voltage (Required)

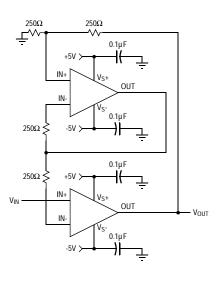
R<sub>L</sub> = Load Resistance

### **Typical Application Circuits**

Inverting 200mA Output Current Distribution Amplifier

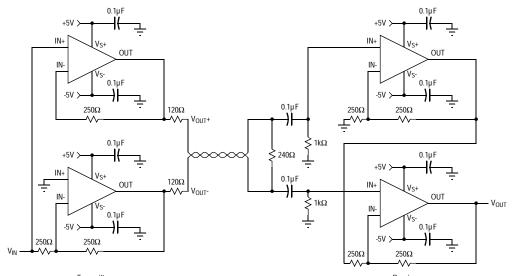


Fast-Settling Precision Amplifier









Transmitter

Receiver

# April 12, 2001

Elantec Semiconductor, Inc. 675 Trade Zone Blvd. Milpitas, CA 95035 Telephone: (408) 945-1323 (888) ELANTEC Fax: (408) 945-9305 European Office: +44-118-977-6020 Japan Technical Center: +81-45-682-5820

herein and makes no representations that they are free from patent infringement.

**General Disclaimer** 

HIGH PERFORMANCE ANALOG INTEGRATED CIRCUITS

#### WARNING - Life Support Policy

Elantec, Inc. products are not authorized for and should not be used within Life Support Systems without the specific written consent of Elantec, Inc. Life Support systems are equipment intended to support or sustain life and whose failure to perform when properly used in accordance with instructions provided can be reasonably expected to result in significant personal injury or death. Users contemplating application of Elantec, Inc. Products in Life Support Systems are requested to contact Elantec, Inc. factory headquarters to establish suitable terms & conditions for these applications. Elantec, Inc.'s warranty is limited to replacement of defective components and does not cover injury to persons or property or other consequential damages.

Specifications contained in this data sheet are in effect as of the publication date shown. Elantec, Inc. reserves the right to make changes in the circuitry or specifications contained herein at any time without notice. Elantec, Inc. assumes no responsibility for the use of any circuits described