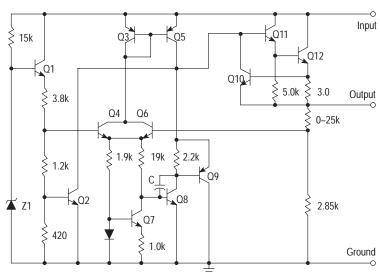
Three-Terminal Low Current Positive Voltage Regulators

The MC78L00A Series of positive voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100 mA. Like their higher powered MC7800 and MC78M00 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the MC78L00 devices in many applications.

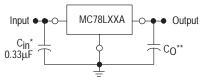
These devices offer a substantial performance advantage over the traditional zener diode–resistor combination, as output impedance and quiescent current are substantially reduced.

- Wide Range of Available, Fixed Output Voltages
- Low Cost
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required
- Complementary Negative Regulators Offered (MC79L00A Series)



Representative Schematic Diagram

Standard Application



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

*C_{in} is required if regulator is located an appreciable distance from power supply filter.

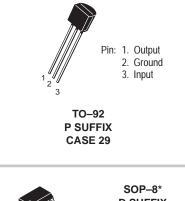
**CO is not needed for stability; however, it does improve transient response.



ON Semiconductor

Formerly a Division of Motorola

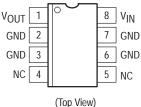






*SOP-8 is an internally modified SO-8 package. Pins 2, 3, 6, and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipationcapability when appropriately mounted on a printed circuit board. SOP-8 conforms to all external dimensions of the standard SO-8 package.





DEVICE TYPE/NOMINAL VOLTAGE

5% Output Voltage Accuracy	Voltage
MC78L05AC	5.0
MC78L08AC	8.0
MC78L09AC	9.0
MC78L12AC	12
MC78L15AC	15
MC78L18AC	18
MC78L24AC	24

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

MAXIMUM RATINGS ($T_A = +125^{\circ}C$, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (2.6 V–8.0 V) (12 V–18 V) (24 V)	VI	30 35 40	Vdc
Storage Temperature Range	T _{stg}	-65 to +150	°C
Operating Junction Temperature Range	Тj	0 to +150	°C

ELECTRICAL CHARACTERISTICS (V_I = 10 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40°C < T_J < +125°C (for MC78LXXAB), 0°C < T_J < +125°C (for MC78LXXAC), unless otherwise noted.)

		M	C78L05AC,	AB	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	VO	4.8	5.0	5.2	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA)	Reg _{line}				mV
7.0 Vdc \leq VI \leq 20 Vdc 8.0 Vdc \leq VI \leq 20 Vdc			55 45	150 100	
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Regload		11 5.0	60 30	mV
$\begin{array}{l} \mbox{Output Voltage} \\ (7.0 \mbox{ Vdc} \leq \mbox{V}_I \leq 20 \mbox{ Vdc}, \ 1.0 \mbox{ mA} \leq \mbox{I}_O \leq 40 \mbox{ mA}) \\ (\mbox{V}_I = 10 \mbox{ V}, \ 1.0 \mbox{ mA} \leq \mbox{I}_O \leq 70 \mbox{ mA}) \end{array}$	Vo	4.75 4.75		5.25 5.25	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		3.8 -	6.0 5.5	mA
Input Bias Current Change (8.0 Vdc \leq V _I \leq 20 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔIB			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz \leq f \leq 100 kHz)	V _n	-	40	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 8.0 Vdc \leq V _I \leq 18 V, T _J = +25°C)	RR	41	49	-	dB
Dropout Voltage ($T_J = +25^{\circ}C$)	VI – VO	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V _I = 14 V, I _O = 40 mA, C _I = 0.33 µF, C _O = 0.1 µF, -40°C < T _J < +125°C (for MC78LXXAB),
0°C < T _J < +125°C (for MC78LXXAC), unless otherwise noted.)

		MC78L08AC, AB			
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	7.7	8.0	8.3	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA)	Reg _{line}				mV
10.5 Vdc \leq V _I \leq 23 Vdc 11 Vdc \leq V _I \leq 23 Vdc			20 12	175 125	
Load Regulation (T _J = +25°C, 1.0 mA ≤ I _O ≤ 100 mA) (T _J = +25°C, 1.0 mA ≤ I _O ≤ 40 mA)	Reg _{load}		15 8.0	80 40	mV
$\begin{array}{l} \label{eq:2.1} \mbox{Output Voltage} \\ \mbox{(10.5 Vdc} \leq V_I \leq 23 \mbox{Vdc}, \ 1.0 \mbox{ mA} \leq I_O \leq 40 \mbox{ mA}) \\ \mbox{(V_I} = 14 \mbox{ V}, \ 1.0 \mbox{ mA} \leq I_O \leq 70 \mbox{ mA}) \end{array}$	Vo	7.6 7.6		8.4 8.4	Vdc
nput Bias Current ($T_J = +25^{\circ}C$) ($T_J = +125^{\circ}C$)	IB		3.0	6.0 5.5	mA
Input Bias Current Change (11 Vdc \leq V _I \leq 23 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f ≤ 100 kHz)	Vn	-	60	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 12 V \leq V _I \leq 23 V, T _J = +25°C)	RR	37	57	-	dB
Dropout Voltage (TJ = +25°C)	VI-VO	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS ($V_I = 15 V$, $I_O = 40 mA$, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, $-40^{\circ}C < T_J < +125^{\circ}C$ (for MC78LXXAB), $0^{\circ}C < T_J < +125^{\circ}C$ (for MC78LXXAC), unless otherwise noted.)

		м	C78L09AC,	AB	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	8.6	9.0	9.4	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA) 11.5 Vdc \leq V _I \leq 24 Vdc 12 Vdc \leq V _I \leq 24 Vdc	Reg _{line}		20 12	175 125	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		15 8.0	90 40	mV
Output Voltage (11.5 Vdc \leq VI \leq 24 Vdc, 1.0 mA \leq I _O \leq 40 mA) (VI = 15 V, 1.0 mA \leq I _O \leq 70 mA)	Vo	8.5 8.5		9.5 9.5	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		3.0 -	6.0 5.5	mA
Input Bias Current Change (11 Vdc \leq V _I \leq 23 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	Vn	-	60	-	μV
$\begin{array}{l} \mbox{Ripple Rejection (I_O = 40 mA,} \\ \mbox{f = 120 Hz, 13 V } \leq V_I \leq 24 \ \mbox{V, T_J = +25^{\circ}C} \end{array} \end{array}$	RR	37	57	-	dB
Dropout Voltage (T _J = +25°C)	VI – VO	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (VI = 19 V, I_O = 40 mA, CI = 0.33 μ F, CO = 0.1 μ F, -40°C < TJ < +125°C (for MC78LXXAB), 0°C < TJ < +125°C (for MC78LXXAC), unless otherwise noted.)

		M	MC78L12AC, AB		
Characteristics	Symbol	Min	Тур	Мах	Unit
Output Voltage (T _J = +25°C)	VO	11.5	12	12.5	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 14.5 Vdc $\leq V_I \leq 27 \text{ Vdc}$ 16 Vdc $\leq V_I \leq 27 \text{ Vdc}$	Reg _{line}		120 100	250 200	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		20 10	100 50	mV
Output Voltage (14.5 Vdc \leq V _I \leq 27 Vdc, 1.0 mA \leq I _O \leq 40 mA) (V _I = 19 V, 1.0 mA \leq I _O \leq 70 mA)	Vo	11.4 11.4		12.6 12.6	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		4.2	6.5 6.0	mA
Input Bias Current Change (16 Vdc \leq V _I \leq 27 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz $\leq f \leq 100$ kHz)	Vn	-	80	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 15 V \leq V _I \leq 25 V, T _J = +25°C)	RR	37	42	-	dB
Dropout Voltage (T _J = +25°C)	VI – VO	-	1.7	-	Vdc

 $\frac{1}{1 + 1} = \frac{1}{1 + 1} =$

	M	C78L15AC,	AB		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	VO	14.4	15	15.6	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA) 17.5 Vdc ≤ V _I ≤ 30 Vdc 20 Vdc ≤ V _I ≤ 30 Vdc	Reg _{line}		130 110	300 250	mV
Load Regulation (T _J = +25°C, 1.0 mA \le I _O \le 100 mA) (T _J = +25°C, 1.0 mA \le I _O \le 40 mA)	Reg _{load}		25 12	150 75	mV
$\begin{array}{l} \mbox{Output Voltage} \\ (17.5 \mbox{ Vdc} \leq V_I \leq 30 \mbox{ Vdc}, \ 1.0 \mbox{ mA} \leq I_O \leq 40 \mbox{ mA}) \\ (V_I = 23 \mbox{ V}, \ 1.0 \mbox{ mA} \leq I_O \leq 70 \mbox{ mA}) \end{array}$	Vo	14.25 14.25		15.75 15.75	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		4.4	6.5 6.0	mA
Input Bias Current Change (20 Vdc \leq V _I \leq 30 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz $\leq f \leq 100$ kHz)	Vn	-	90	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 18.5 V ≤ V _I ≤ 28.5 V, T _J = +25°C)	RR	34	39	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	VI – VO	-	1.7	-	Vdc

			MC78L18A	C	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	17.3	18	18.7	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 21.4 Vdc $\leq V_I \leq 33 \text{ Vdc}$ 20.7 Vdc $\leq V_I \leq 33 \text{ Vdc}$ 22 Vdc $\leq V_I \leq 33 \text{ Vdc}$ 21 Vdc $\leq V_I \leq 33 \text{ Vdc}$	Reg _{line}		45 35	325 275	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		30 15	170 85	mV
$\begin{array}{l} \text{Output Voltage} \\ (21.4 \ \text{Vdc} \leq V_I \leq 33 \ \text{Vdc}, \ 1.0 \ \text{mA} \leq I_O \leq 40 \ \text{mA}) \\ (20.7 \ \text{Vdc} \leq V_I \leq 33 \ \text{Vdc}, \ 1.0 \ \text{mA} \leq I_O \leq 40 \ \text{mA}) \\ (V_I = 27 \ \text{V}, \ 1.0 \ \text{mA} \leq I_O \leq 70 \ \text{mA}) \\ (V_I = 27 \ \text{V}, \ 1.0 \ \text{mA} \leq I_O \leq 70 \ \text{mA}) \end{array}$	Vo	17.1 17.1	-	18.9 18.9	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		3.1 _	6.5 6.0	mA
Input Bias Current Change (22 Vdc \leq V _I \leq 33 Vdc) (21 Vdc \leq V _I \leq 33 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔIB			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz \leq f \leq 100 kHz)	V _n	-	150	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 23 V \leq V _I \leq 33 V, T _J = +25°C)	RR	33	48	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	VI – VO	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (VI = 27 V, IO = 40 mA, CI = 0.33 μ F, CO = 0.1 μ F, 0°C < TJ < +125°C, unless otherwise noted.)

ELECTRICAL CHARACTERISTICS (VI = 33 V, IO = 40 mA, CI = 0.33 μ F, CO = 0.1 μ F, 0°C < TJ < +125°C, unless otherwise noted.)

			MC78L24A	C	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	23	24	25	Vdc
Line Regulation (T _J = +25°C, I_O = 40 mA)	Reg _{line}				mV
$27.5 \text{ Vdc} \le \text{V}_{I} \le 38 \text{ Vdc}$ $28 \text{ Vdc} \le \text{V}_{I} \le 80 \text{ Vdc}$ $27 \text{ Vdc} \le \text{V}_{I} \le 38 \text{ Vdc}$		_ _ _	- 50 60	_ 300 350	
Load Regulation (T _J = +25°C, 1.0 mA \le I _O \le 100 mA) (T _J = +25°C, 1.0 mA \le I _O \le 40 mA)	Reg _{load}		40 20	200 100	mV
$\begin{array}{l} Output \mbox{ Voltage} \\ (28 \mbox{ Vdc} \le V_I \le 38 \mbox{ Vdc}, \ 1.0 \ mA \le I_O \le 40 \ mA) \\ (27 \mbox{ Vdc} \le V_I \le 38 \mbox{ Vdc}, \ 1.0 \ mA \le I_O \le 40 \ mA) \\ (28 \mbox{ Vdc} \le V_I = 33 \mbox{ Vdc}, \ 1.0 \ mA \le I_O \le 70 \ mA) \\ (27 \mbox{ Vdc} \le V_I \le 33 \mbox{ Vdc}, \ 1.0 \ mA \le I_O \le 70 \ mA) \end{array}$	Vo	22.8 22.8		25.2 25.2	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	IB		3.1 _	6.5 6.0	mA
Input Bias Current Change (28 Vdc \leq V _I \leq 38 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz $\leq f \leq 100$ kHz)	V _n	-	200	-	μV
$\begin{array}{l} \mbox{Ripple Rejection (I_O = 40 mA,} \\ \mbox{f = 120 Hz, 29 V } \le \ \mbox{V}_I \le 35 \ \mbox{V}, \ \mbox{T}_J = +25^{\circ}\mbox{C}) \end{array}$	RR	31	45	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	VI – VO	-	1.7	-	Vdc

5.0

4.0

3.0

2.0

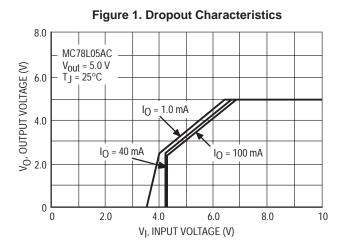


Figure 2. Dropout Voltage versus **Junction Temperature**

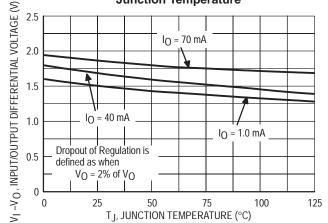


Figure 4. Input Bias Current

versus Input Voltage

MC78L05AC

 $V_{out} = 5.0 V$ $I_0 = 40 \text{ mA}$

Tj = 25°C

Figure 3. Input Bias Current versus **Ambient Temperature**

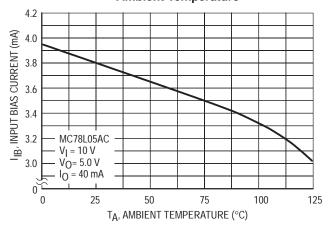


Figure 5. Maximum Average Power Dissipation versus

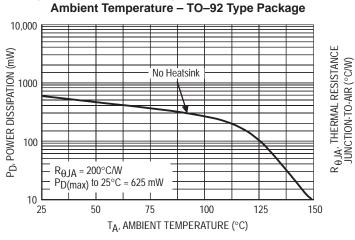
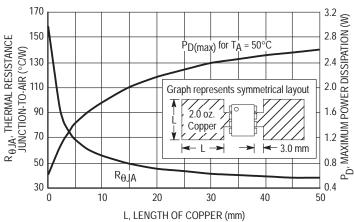
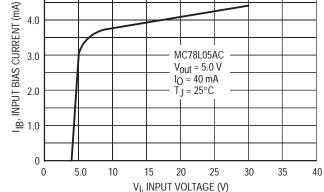


Figure 6. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length





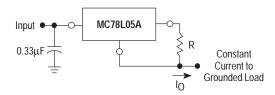
APPLICATIONS INFORMATION

Design Considerations

The MC78L00A Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. The





The MC78L00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC78L05C is chosen in this application. Resistor R determines the current as follows:

$$I_{O} = \frac{5.0 \text{ V}}{\text{R}} + I_{B}$$

I_{IB} = 3.8 mA over line and load changes

For example, a 100 mA current source would require R to be a 50 Ω , 1/2 W resistor and the output voltage compliance would be the input voltage less 7 V.

input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μ F or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen.The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.

Figure 8. ± 15 V Tracking Voltage Regulator

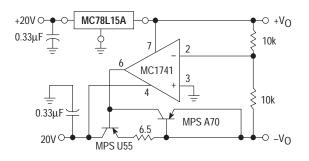
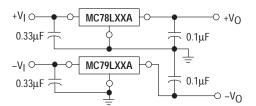


Figure 9. Positive and Negative Regulator



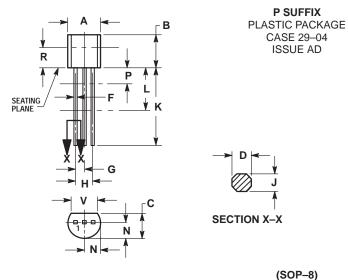
ORDERING INFORMATION

Device	Operating Temperature Range	Package	Shipping
MC78LXXACD*		SOP-8	98 Units / Rail
MC78LXXACDR2*		SOP–8 / Tape & Reel	2500 Units / Tape & Reel
MC78LXXACP	T _J = 0° to +125°C	TO-92	2000 Units / Bag
MC78LXXACPRA / MC78LXXACPRE	1	TO–92 / Tape & Reel	2000 Units / Tape & Reel
MC78LXXACPRM / MC78LXXACPRP	1	TO–92 / Ammo Pack	2000 Units / Ammo Pack
MC78LXXABD*		SOP-8	98 Units / Rail
MC78LXXABDR2*]	SOP–8 / Tape & Reel	2500 Units / Tape & Reel
MC78LXXABP*	T _J = -40° to +125°C	TO-92	2000 Units / Bag
MC78LXXABPRA / MC78LXXABPRE	1	TO–92 / Tape & Reel	2000 Units / Tape & Reel
MC78LXXABPRM / MC78LXXABPRP		TO–92 / Ammo Pack	2000 Units / Ammo Pack

XX indicates nominal voltage

*Available in 5, 8, 9, 12 and 15 V devices.

PACKAGE DIMENSIONS



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 2 CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L DIMENSION D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM

	INC	INCHES		IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
К	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	

D SUFFIX PLASTIC PACKAGE CASE 751-05 ISSUE R A n С h ф ⊕ 0.25 M BM Ε н Н h x 45 В е С SEATING PLANE 0.10 \cap A1 в 0.25 M C B S A S \oplus

- NOTES DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 1.
 - DIMENSIONS ARE IN MILLIMETERS
- DIMENSION D AND E DO NOT INCLUDE MOLD 3
- PROTRUSION. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE. 4
- 5 DIMENSION B DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS	
DIM	MIN	MAX
Α	1.35	1.75
A1	0.10	0.25
В	0.35	0.49
С	0.18	0.25
D	4.80	5.00
E	3.80	4.00
e	1.27 BSC	
Н	5.80	6.20
h	0.25	0.50
L	0.40	1.25
θ	0 °	7 °

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