

LOW POWER

FM TRANSMITTER

SYSTEM

SEMICONDUCTOR

TECHNICAL DATA

Low Power FM Transmitter System

MC2833 is a one-chip FM transmitter subsystem designed for cordless telephone and FM communication equipment. It includes a microphone amplifier, voltage controlled oscillator and two auxiliary transistors.

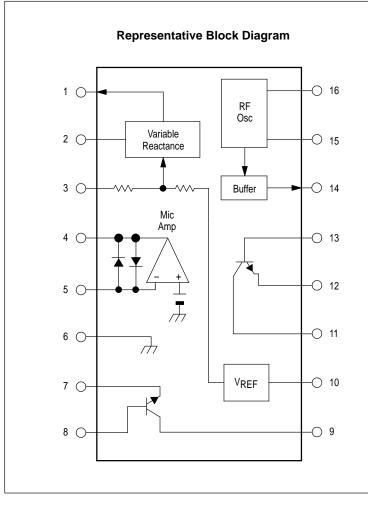
- Wide Range of Operating Supply Voltage (2.8–9.0 V)
- Low Drain Current (ICC = 2.9 mA Typ)
- Low Number of External Parts Required
- - 30 dBm Power Output to 60 MHz Using Direct RF Output
- +10 dBm Power Output Attainable Using On–Chip Transistor Amplifiers
- Users Must Comply with Local Regulations on R.F. Transmission (FCC, DOT, P.T.T., etc)

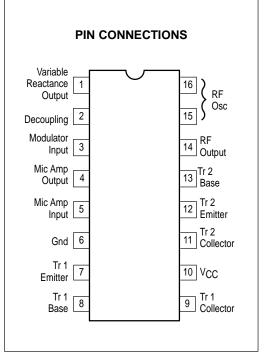




P SUFFIX PLASTIC PACKAGE CASE 648

D SUFFIX PLASTIC PACKAGE CASE 751B (SO-16)





ORDERING INFORMATION

Device	Operating Temperature Range	Package			
MC2833D	T. 00 to . 7500	SO-16			
MC2833P	$T_A = -30 \text{ to } +75^{\circ}\text{C}$	Plastic DIP			

MAXIMUM RATINGS

Ratings	Symbol	Value	Unit
Power Supply Voltage	VCC	10 (max)	V
Operating Supply Voltage Range	VCC	2.8–9.0	V
Junction Temperature	ТJ	+ 150	°C
Operating Ambient Temperature	TA	- 30 to + 75	°C
Storage Temperature Range	T _{stg}	– 65 to + 150	°C

ELECTRICAL CHARACTERISTICS (V_{CC} = 4.0 V, T_A = 25°C, unless otherwise noted)

Characteristics	Symbol	Pin	Min	Тур	Max	Unit
Drain Current (No input signal)	ICC	10	1.7	2.9	4.3	mA
FM MODULATOR						
Output RF Voltage (f ₀ = 16.6 MHz)	Vout RF	14	60	90	130	mVrms
Output DC Voltage (No input signal)	Vdc	14	2.2	2.5	2.8	V
Modulation Sensitivity (f ₀ = 16.6 MHz) (V _{in} = 0.8 V to 1.2 V)	SEN	3 14	7.0 -	10 -	15 -	Hz/mVdc
Maximum Deviation ($f_0 = 16.6 \text{ MHz}$) ($V_{in} = 0 \text{ V to } 2.0 \text{ V}$)	Fdev	3 14	3.0 _	5.0 -	10 -	kHz
MIC AMPLIFIER						-
Closed Loop Voltage Gain (V _{in} = 3.0 mVrms) (f _{in} = 1.0 kHz)	A _V	4 5	27 -	30 -	33 -	dB
Output DC Voltage (No input signal)	V _{out} dc	4	1.1	1.4	1.7	V
Output Swing Voltage (V _{in} = 30 mVrms) (f _{in} = 1.0 kHz)	V _{out} p–p	4	0.8	1.2	1.6	Vр–р
Total Harmonic Distortion (V _{in} = 3.0 mVrms) (f _{in} = 1.0 kHz)	THD	4	-	0.15	2.0	%

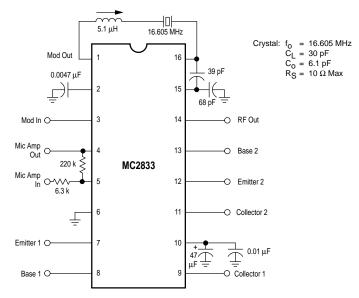
AUXILIARY TRANSISTOR STATIC CHARACTERISTICS

Characteristics	Symbol	Min	Тур	Max	Unit
Collector Base Breakdown Voltage ($I_C = 5.0 \mu A$)	V _(BR) CBO	15	45	1	V
Collector Emitter Breakdown Voltage (I _C = 200 μ A)	V _(BR) CEO	10	15	-	V
Collector Substrate Breakdown Voltage ($I_C = 50 \mu A$)	V _(BR) CSO	_	70	-	V
Emitter Base Breakdown Voltage (I _E = 50 μ A)	V _{(BR)EBO}	_	6.2	-	V
Collector Base Cut Off Current (V _{CB} = 10 V) (I _E = 0)	СВО	-	-	200	nA
DC Current Gain (I _C = 3.0 mA) (V _{CE} = 3.0 V)	hFE	40	150	-	-

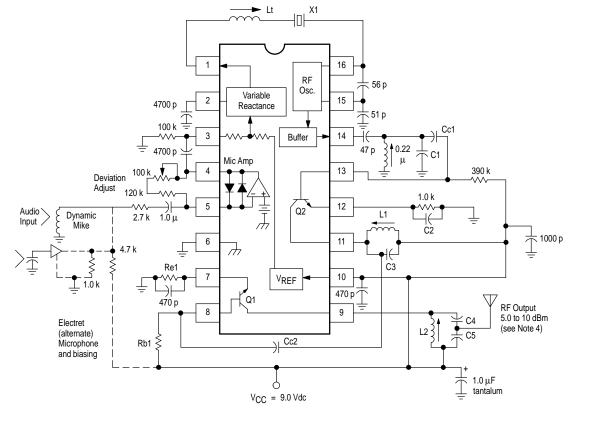
AUXILIARY TRANSISTOR DYNAMIC CHARACTERISTICS

Current Gain Bandwidth Product ($V_{CE} = 3.0 \text{ V}$) ($I_C = 3.0 \text{ mA}$)	fΤ	-	500	-	MHz
Collector Base Capacitance ($V_{CE} = 3.0 V$) ($I_C = 0$)	C _{CB}	-	2.0	-	pF
Collector Substrate Capacitance ($V_{CS} = 3.0 \text{ V}$) ($I_{C} = 0$)	C _{CS}	-	3.3	-	pF

MC2833 Figure 1. Test Circuit







NOTES:

1. Components versus output frequency:

Output RF	<u>X1 (MHz)</u>	<u>Lt (μH</u>)	<u>L1 (μH</u>)	L2 (μ H)	Re1	Rb1	Cc1	Cc2	C1	C2	C3	C4	C5
49.7 MHz	16.5667	3.3-4.7	0.22	0.22	330	390 k	33 p	33 p	33 p	470 p	33 p	47 p	220 p
76 MHz	12.6000	5.1	0.22	0.22	150	300 k	68 p	10 p	68 p	470 p	12 p	20 p	120 p
144.6 MHz	12.05	5.6	0.15	0.10	150	220 k	47 p	10 p	68 p	1000 p	18 p	12 p	33 p

 Crystal X1 is fundamental mode, calibrated for parallel resonance with a 32 pF load. The final output frequency is generated by frequency multiplication within the MC2833 IC. The RF output buffer (Pin 14) and Q2 transistor are used as a frequency tripler and doubler, respectively, in the 76 and 144.6 MHz transmitters. The Q1 output transistor is a linear amplifier in the 49.7 MHz and 76 MHz transmitters, and a frequency doubler in the 144.6 MHz transmitter.

3. All coils used are 7 mm shielded inductors, CoilCraft series M1175A, M1282A-M1289A, M1312A or equivalent.

 Power output is ≈ + 10 dBm for 49.7 MHz and 76 MHz transmitters, and ≈ + 5.0 dBm for the 144.6 MHz transmitter at V_{CC} = 8.0 V. Power output drops with lower V_{CC}.

5. All capacitors in microfarads, inductors in Henries and resistors in Ohms unless otherwise specified.

6. Other frequency combinations may be set-up by simple scaling of the 3 examples shown.

Figure 3. Buffer/Multiplier (x3, Pin 14) (16 MHz Fundamental)

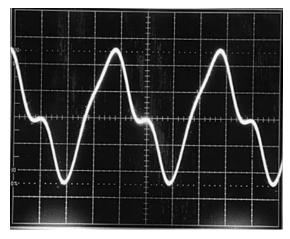


Figure 5. Doubler Output 76 MHz (Pin 11)

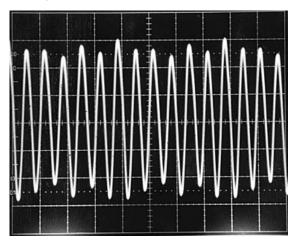


Figure 4. Input to Doubler (Pin 13) (49.7 MHz x 3 Component)

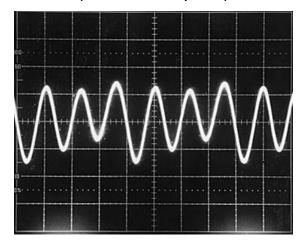


Figure 6. Spectrum

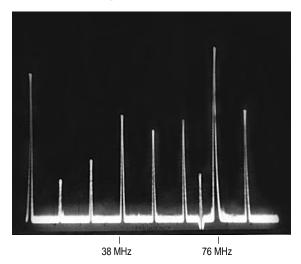


Figure 7. Output Spectrum (49.7 MHz)

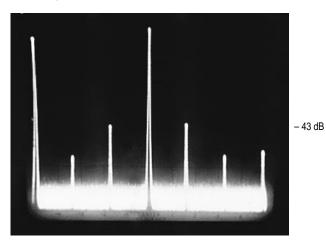


Figure 8. Modulation Spectrum (1.0 kHz Showing Carrier Null)

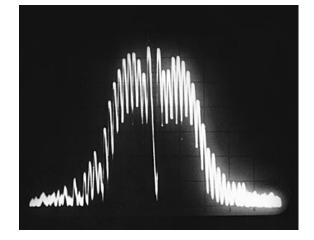


Figure 9. 144.6 MHz/x12 Multiplier

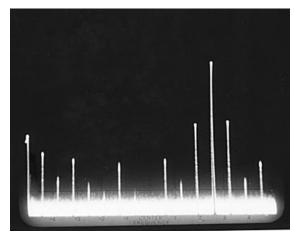


Figure 10. Circuit Side View

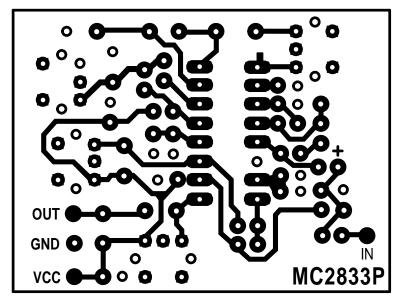


Figure 11. Ground Plane on Component Side

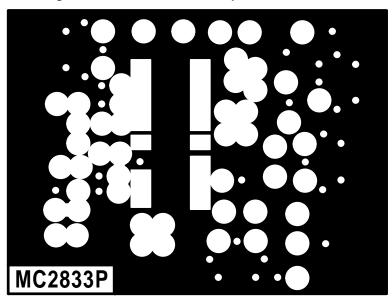
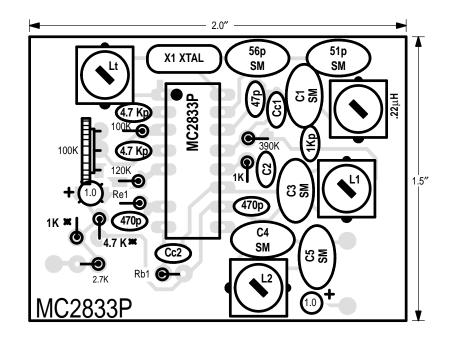
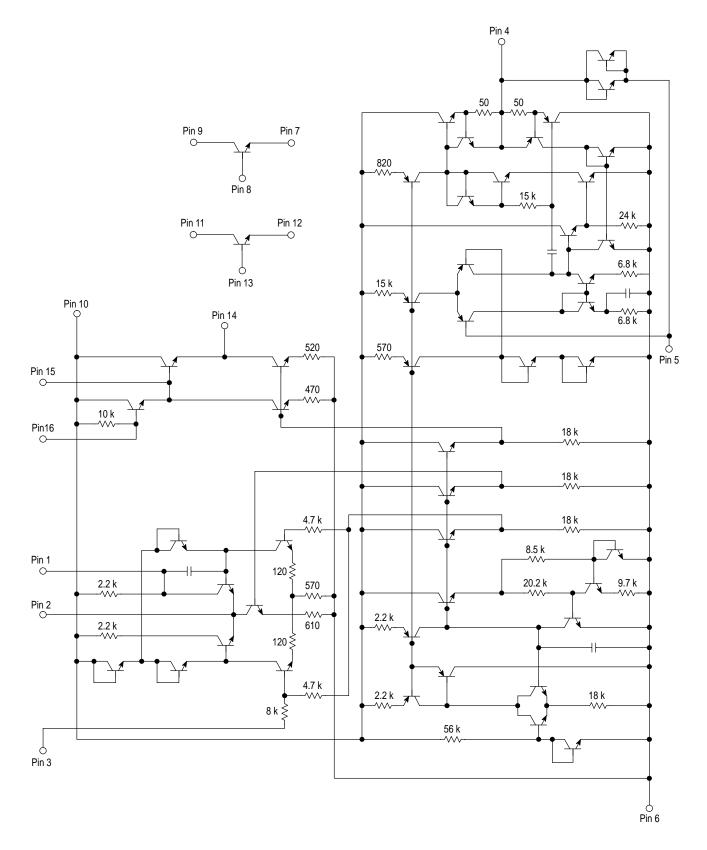


Figure 12. Component View

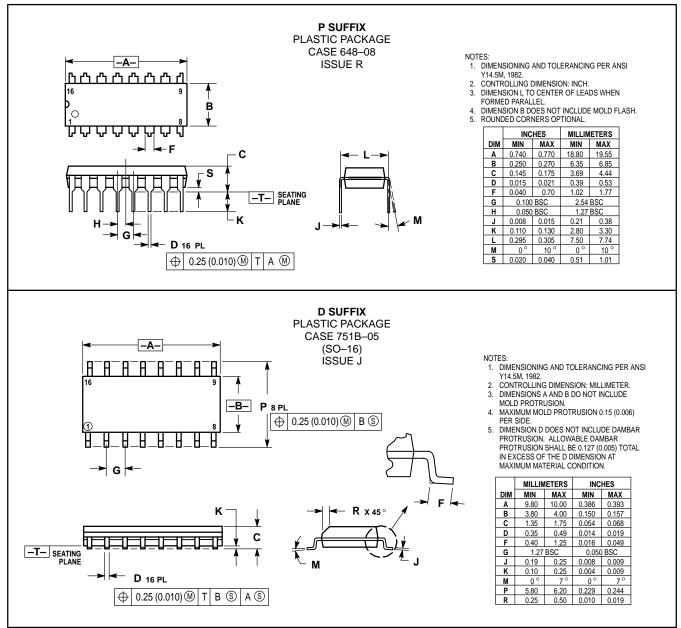


- NOTES: Positive artwork provided.
 Drill holes must be plated to ensure making all ground (V_{EE}) connections!
 Resistors labelled * are used for biasing of electret microphone if used.
 Capacitors labelled "SM" are silver mica.
 Final board size 1.5" × 2.0".

Figure 13. Circuit Schematic



OUTLINE DIMENSIONS



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