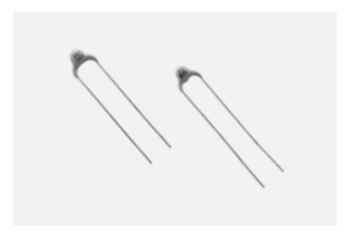
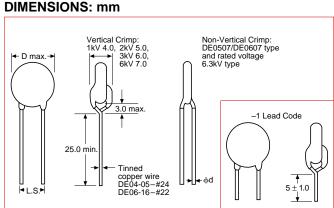
LEADED CAPACITORS, NETWORKS & HV CAPACITORS MEDIUM VOLTAGE CAPACITORS

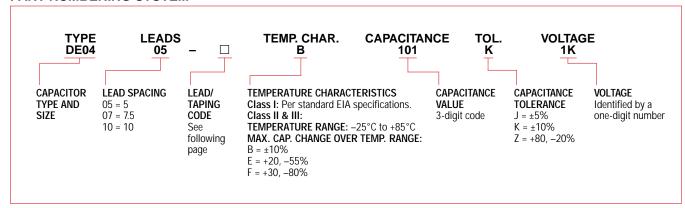
1kV to 6kVDC E.I.A. CLASS II & III



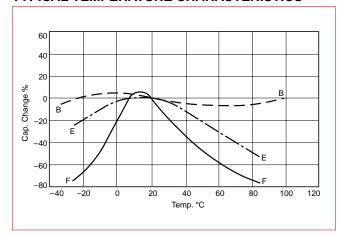




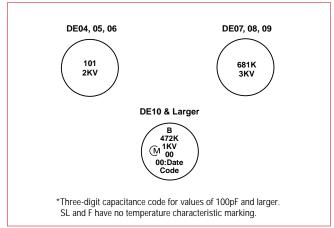
PART NUMBERING SYSTEM



TYPICAL TEMPERATURE CHARACTERISTICS



MARKING*



LEADED CAPACITORS, NETWORKS & HV CAPACITORS MEDIUM VOLTAGE CAPACITORS 1kV to 6kVDC E.I.A. CLASS || & |||



B CHARACTERISTIC (Similar to EIA Temperature Characteristic Y5P)

	•						☐: Lead Configu	ration/Lead Cod	e	
Dort Marchan		Men				Straight Short	Double Crimp		Taping* [†]	
Part Number (□: optional lead code shown at the right)	Nominal Capacitance (pF)	Max. Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (kVDC)	Long	\bigcap	<u>.</u>	Lead Spacing F: 5.0 Pitch of Component P: 12.7	Lead Spacing F: 7.5 Pitch of Component P: 15.0	loloolo Lead Spacing F: 7.5 Pitch of Component P: 30.0
DE0405□B101K1K DE0405□B151K1K DE0405□B221K1K DE0405□B331K1K	100 150 220 330	4.5								
DE0505 B471K1K	470	5	5.0					- 979	_	
DE0605□B681K1K DE0605□B102K1K	680 1000	6		1			-69			_
DE0805□B152K1K	1500	8								
DE0905□B222K1K	2200	9								
DE1005□B332K1K	3300	10								
DE1207□B472K1K	4700	12	7.5				/10	-	-486	477
DE1507 B682K1K	6800	15					-610	-		-477
DE0405□B101K2K DE0405□B151K2K DE0405□B221K2K	100 150 220	4.5								
DE0505□B331K2K	330	5							_	
DE0605□B471K2K	470	6	5.0	2	No Code	– 1	_	-979	_	_
DE0705□B681K2K	680	7								
DE0805 B102K2K	1000	8	7.5							
DE0905□B152K2K DE1005□B222K2K	1500 2200	9 10								
DE1207□B332K2K	3300	12							-486	
DE1507□B472K2K	4700	15					-610	1	-	-477
DE0507□B101K3K DE0507□B151K3K DE0507□B221K3K	100 150 220	5								
DE0607□B331K3K	330	6								
DE0707□B471K3K	470	7	7.5	3.15			-620		-486	_
DE0807□B681K3K	680	8	7.5	3.10						
DE0907□B102K3K	1000	9								
DE1107□B152K3K	1500	11						_		
DE1307□B222K3K	2200	13					/10	-		477
DE1507□B332K3K DE0910□B101K6K	3300 100	15					-610	4		-477
DE0910□B101K6K DE0910□B151K6K DE0910□B221K6K	150 150 220	9			No Code					
DE0910□B331K6K	330		10.0	6.3	Bulk Only	_	_		_	_
DE1010 B471K6K	470	10								
DE1110□B681K6K	680	11								
DE1310 B102K6K E CHARACTE	1000 RISTIC (S	13 imilar to E	EIA Temp	erature C	haracteristic	Y5U)			*900	pcs. for KH type
DE0505□E102Z1K	1000	5								
DE0705□E222Z1K	2200	7	5.0	1			-69	-979	_	
DE0905 E472Z1K	4700	9	7	·			/00		407	
DE1307□E103Z1K	10000	13	7.5	7.5			-620	_	-486	_
DE0605□E102Z2K DE0805□E222Z2K	1000 2200	6 8	5.0		No Code	-1	_	-979		
DE1105□E472Z2K	4700	11	5.0	2	NO COUC	-1	_	-7/7	_	
DE1607□E103Z2K	10000	16	7.5				-610	 		-477
DE0707□E102Z3K	1000	7	7.0		1		310	1		17.7
DE1007□E222Z3K	2200	10	7.5	3.15			-620		-486	
DE1307□E472Z3K	4700	13						_		_
DL 1307 LL47223N								1		1
DE1110□E102Z6K	1000	11	10.0	6.3	Bulk Only		_		_	

F CHARACTERISTIC (Similar to EIA Temperature Characteristic Y5V)

					☐: Lead Configuration/Lead Code							
Dowl Novel on					Straight Long	Straight Short	Double Crimp	Tapi	ng*⁺			
Part Number (: optional lead code shown at the right)	Nominal Capacitance (pF)	Max. Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (kVDC)				Lead Spacing F: 5.0 Pitch of Component P: 12.7	Lead Spacing F: 7.5 Pitch of Component P: 15.0			
DE0605□F222Z1K	2200	6										
DE0705□F472Z1K	4700	7	5.0	1			-69					
DE1005□F103Z1K	10000	10						-979	_			
DE0505□F102Z2K	1000	5			No Code	-1		717	_			
DE0705□F222Z2K	2200	7	5.0	2			_					
DE0905□F472Z2K	4700	9										
DE1207□F103Z2K	10000	12	7.5					_	-486			





LEADED CAPACITORS, NETWORKS & HV CAPACITORS MEDIUM VOLTAGE CAPACITORS 1kV to 6kVDC E.I.A. CLASS II & III–SPECIFICATIONS & TEST METHODS DE Series

	Item		Specificat	ion	Test Method		
			Temperature Compensating	High Dielectric Constant	rest inethod		
1	Operating Temper Capacitance	rature Range	-25 to +85°C Within the specified tolerance.	–25 to +85°C Within the specified	The capacitance shall be measured at 20°C with 1 ± 0.2kHz		
	Q		'	tolerance.	(SL: 1 ± 0.2MHz) and 5V(r.m.s.) max.		
3	Dissipation Factor (D.F.)		SL $C \ge 30pF: Q \ge 1000$ $C < 30pF: Q \ge 400 + 20C^1$	B, E D.F. ≤ 2.5% F D.F. ≤ 5.0%	Same condition as capacitance.		
4	Insulation Resistance (I.R.)	Between lead wires	10000M ohms min.	10000M ohms min.	The insulation resistance shall be measured with 500 \pm 50VDC wit 60 \pm 5 sec. of charging.		
5	Dielectric Strength	Between lead wires	No failure.	No failure.	The capacitors shall not be damaged when DC voltage of 200% of the rated voltage is applied between the lead wires for 1 to 5 sec. (Charge/discharge current ≤50mA)		
		Body Insulation	No failure.	No failure.	The capacitor is placed in the container with metal balls of diameter 1mm so that each lead wire, short-circuited, is kept approximately 2mm off the balls as shown in the figure, and DC voltage of 1.3kV is applied for 1 to 5 sec. between capacitor lead wires and small metals. (Charge/discharge current <50mA).		
6	Temperature Characteristic		T.C. Temp. Coefficient SL +350 to -1000ppm/°C	T.C. Cap. Change	The capacitance measurement shall be made at each step specified in table. Capacitance change from the value of step 3 shall not exceed the limit specified.		
7	Vibration	Annograpas	No marked defeat	No marked defect	condition ² for 24 ± 2 hours before initial measurements (B, E, F).		
'	Vibration Resistance	Appearance Capacitance Change	No marked defect. Within the specified tolerance.	No marked defect. Within the specified tolerance.	The capacitor shall firmly be soldered to the supporting lead wire and vibration which is 10 to 55Hz in the vibration frequency range, 1.5mm in total amplitude, and about 1 minute in the rate of vibration change		
		Q. D.F.	SL C≥30pF: Q≥1000 C<30pF: Q≥400+20C ¹	B, E D.F. ≤ 2.5% F D.F. ≤ 5.0%	from 10Hz to 55Hz and back to 10Hz is applied for a total of 6 hours; 2 hours each in 3 mutually perpendicular directions.		
8	Soldering	Appearance	No marked defect.	No marked defect.	The lead wire shall be immersed into the melted solder of 350 \pm 10°C		
	Effect	Capacitance Change	SL within ±2.5%	B within ±5% E within ±15% F within ±20%	(body of ϕ 5 and under: 270 \pm 5°C) up to about 1.5 to 2mm from the main body for 3.5 \pm 0.5 sec. (body of ϕ 5 and under: 5 \pm 0.5 sec.). Pre-treatment Capacitor shall be stored at 85 \pm 2°C for 1 hour, then placed at room		
		Dielectric Strength (between lead wires)	Pass the item No. 5.	Pass the item No. 5.	condition ² for 24 ± 2 hours before initial measurements (B, E, F). Post-treatment Capacitor shall be stored for 1 to 2 hours at room condition ² (SL). Capacitor shall be stored for 24 ± 2 hours at room condition ² (B, E, F).		
9	Humidity	Appearance	No marked defect.	No marked defect.	Set the capacitor for 500 $^{+24}_{-0}$ hours at 40 \pm 2°C in 90 to 95% humidity.		
	(Under Steady State)	Capacitance Change	SL within ±5%	B within ±10% E within ±20% F within ±30%	Pre-treatment Capacitor shall be stored at 85 ± 2°C for 1 hour, then placed at room condition ² for 24 ± 2 hours before initial measurements (B, E, F). Post-treatment		
		Q. D.F.	SL $C \ge 30pF: Q \ge 350$ $C < 30pF: Q \ge 275 + \frac{5}{2}C^{1}$	B, E D.F. ≤ 5.0% F D.F. ≤ 7.5%	Capacitor shall be stored for 1 to 2 hours at room condition ² .		
		I.R.	1000M ohms min.	1000M ohms min.	A 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
10	Humidity Loading	Appearance Capacitance	No marked defect.	No marked defect.	Apply the rated voltage for 500 ⁺² / ₀ hours at 40 ± 2°C in 90 to 95% humidity. (Charge/discharge current ≤50mA).		
	,	Change	SL within ±7.5%	B within ±10% E within ±20% F within ±30%	Pre-treatment Capacitor shall be stored at 85 ± 2°C for 1 hour, then placed at room condition ² for 24 ± 2 hours before initial measurements (B, E, F).		
		Q. D.F.	SL $C \ge 30pF: Q \ge 200$ $C < 30pF: Q \ge 100 + \frac{10}{3}C^{1}$	B, E D.F. ≤ 5.0% F D.F. ≤ 7.5%	Post-treatment Capacitor shall be stored for 1 to 2 hours at room condition ² (SL). Capacitor shall be stored at 85 ± 2°C for 1 hour, then placed at room		
		I.R.	500M ohms min.	500M ohms min.	condition ² for 24 ± 2 hours (B, E, F).		
11	Life	Appearance	No marked defect.	No marked defect.	Apply a DC voltage of 150% of the rated voltage for 1000 $^{+48}_{-0}$ hours at 85 \pm 2°C. (Charge/discharge current \leq 50mA).		
		Capacitance Change	SL within ±3%	B within ±10% E within ±20% F within ±30%	Pre-treatment Capacitor shall be stored at 85 ± 2°C for 1 hour, then placed at room condition ² for 24 ± 2 hours before initial measurements (B, E, F).		
		Q. D.F.	SL $C \ge 30pF: Q \ge 350$ $C < 30pF: Q \ge 275 + \frac{5}{2}C^{1}$	B , E D.F. ≤ 4.0% F D.F. ≤ 7.5%	Post-treatment Capacitor shall be stored for 1 to 2 hours at room condition ² (SL). Capacitor shall be stored at 85 ± 2°C for 1 hour, then placed at room		
		I.R.	2000M ohms min.	2000M ohms min.	condition ² for 24 ± 2 hours (B, E, F).		
12	Strength of Lead	Pull	Lead wire shall not cut off. Capacitor shall not be broken.		As in figure, fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N (1.0kgf) 5N (0.51kgf) for lead diameter φ0.5, and wtill the state of the first 10.3 capacitor.		
		Bending			Each lead wire shall be subjected to 5N (0.51kgf) 2.5N (0.25kgf) for lead diameter φ0.5 weight and then a 90° bend, at the point of egress, in on direction, return to original position, and then a 90° bend in the opposit		
13	Solderability of Lo		Lead wire shall be soldered with u direction over 3/4 of the circumfere	ential direction.	The lead wire of a capacitor shall be dipped into a methanol solution of 25wt% rosin and then into molten solder of 235 ± 5 °C for 2 ± 0.5 seconds. In both cases the depth of dipping is up to about 1.5 to 2mm from the root of lead wires.		

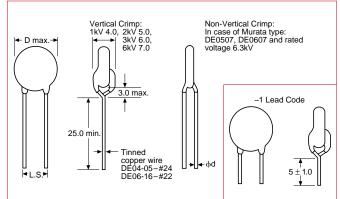
LEADED CAPACITORS, NETWORKS & HV CAPACITORS HIGH TEMPERATURE CAPACITORS

250V to 3kVDC E.I.A. CLASS I & II

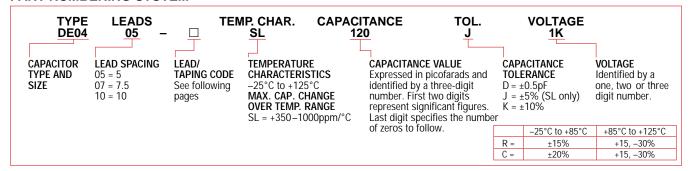




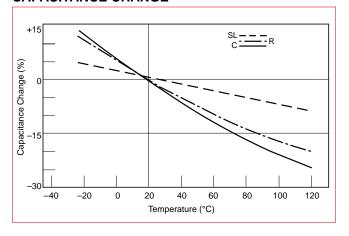
DIMENSIONS: mm



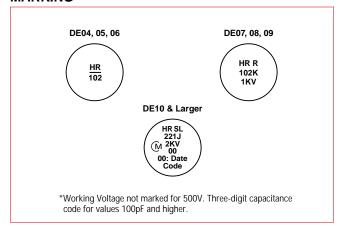
PART NUMBERING SYSTEM



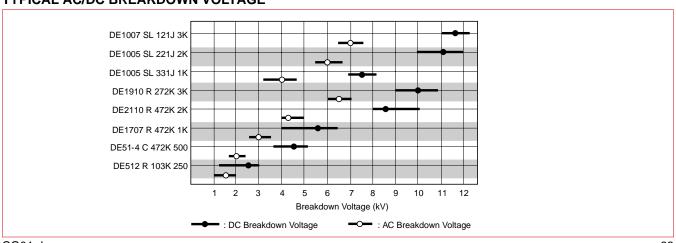
TYPICAL TEMPERATURE VS. **CAPACITANCE CHANGE**



MARKING*



TYPICAL AC/DC BREAKDOWN VOLTAGE



LEADED CAPACITORS, NETWORKS & HV CAPACITORS HIGH TEMPERATURE CAPACITORS 250V to 3kVDC E.I.A. CLASS I & II



SL CHARACTERISTIC

SL CHARACT						[☐: Lead Configure	ration/Lead Cod	e											
					Straight	Straight	Double		Taping* [†]											
Part Number (□: optional lead code shown at the right)	Nominal Capacitance (pF)	Max. Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (kVDC)	Long	Short	Crimp	b o o Lead Spacing F: 5.0	Lead Spacing F: 7.5	o o o o Lead Spacing F: 7.5										
							\ \ \	Pitch of Component P: 12.7	Pitch of Component P: 15.0	Pitch of Component P: 30.0										
DE0405 SL100D1K DE0405 SL120J1K DE0405 SL150J1K DE0405 SL180J1K DE0405 SL220J1K DE0405 SL270J1K DE0405 SL330J1K DE0405 SL390J1K DE0405 SL370J1K	10 12 15 18 22 27 33 39	4.5																		
DE0505□SL560J1K DE0505□SL680J1K	56 68	5	5.0	1			-69	- 979	_											
DE0605□SL820J1K DE0605□SL101J1K DE0605□SL121J1K	82 100 120	6		'																
DE0705□SL151J1K DE0705□SL181J1K	150 180	7																		
DE0805□SL221J1K DE0905□SL271J1K	220 270	8 9																		
DE1005□SL331J1K DE1005□SL391J1K	330 390	10																		
DE1105□SL471J1K	470	11	7.5				(20		407	_										
DE1207□SL561J1K DE0405□SL100D2K	560 10	12	7.5			-1	-620	_	-486											
DE0405 SL120J2K DE0405 SL150J2K DE0405 SL180J2K DE0405 SL220J2K DE0405 SL220J2K DE0405 SL230J2K	12 15 18 22 27 33	4.5																		
DE0505□SL390J2K	39	5	F.0					070												
DE0605□SL470J2K DE0605□SL560J2K	47 56	6	5.0				-69	-979	_											
DE0605□SL680J2K DE0705□SL820J2K	68 82	7		2	No Code															
DE0705□SL101J2K DE0805□SL121J2K	100 120																			
DE0805□SL151J2K DE0905□SL181J2K	150 180	8																		
DE1005□SL221J2K DE1107□SL271J2K	220 270	10 11																		
DE1207□SL331J2K	330	12	7.5																-486	
DE1307□SL391J2K DE1407□SL471J2K	390 470	13 14	7.5				-620		_	-477										
DE1507□SL561J2K DE0507□SL100D3K	560 10	15								1,,										
DE0507□SL120J3K DE0507□SL150J3K DE0507□SL180J3K	12 15 18	5																		
DE0507□SL220J3K DE0607□SL270J3K	22																			
DE0607□SL330J3K DE0607□SL390J3K DE0707□SL470J3K	27 33 39 47	6 7						_	-486	_										
DE0707□SL560J3K DE0807□SL680J3K	. 56 68	8	7.5	3.15		_	_													
DE0807□SL820J3K DE0907□SL101J3K	. 82 100	9																		
DE1007□SL121J3K DE1107□SL151J3K	120 150	10																		
DE1107□SL131J3K DE1107□SL181J3K DE1207□SL221J3K	180 180 220	11 12																		
DE1407□SL271J3K	270	14								477										
DE1507□SL331J3K DE1607□SL391J3K	330 390	15 16							_	-477										

R CHARACTERISTIC (250V)

*900 pcs. for KH type

	·				☐: Lead Configuration/Lead Code						
					Straight Long	Straight Short	Double Crimp	Taping* [↑]			
Part Number (: optional lead code shown at the right)	Nominal Capacitance (pF)	Max. Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (VDC)		\bigcap		Lead Spacing F: 5.0 Pitch of Component P: 12.7			
DE506□R221K250	220										
DE506□R331K250	330	,									
DE506□R471K250	470	6									
DE506 R681K250	680										
DE506□R102K250	1000	_		050		_		070			
DE507□R152K250	1500	7	5.0	250	No Code	– 1	-69	-979			
DE508□R222K250	2200	8									
DE509 R332K250	3300	9									
DE510□R472K250	4700	10									
DE512□R682K250	6800	12									
DE512□R103K250	10000	12									

LEADED CAPACITORS, NETWORKS & HV CAPACITORS HIGH TEMPERATURE CAPACITORS 250V to 3kVDC E.I.A. CLASS I & II



R CHARACTERISTIC (1 to 3 15kV)

		☐: Lead Configuration/Lead Code Straight Straight Double Table 1							e		
Part Number		Max.		Detect Control		Straight Short	Double Crimp		Taping* [†]		
(: optional lead code shown at the right)	Nominal Capacitance (pF)	Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (kVDC)				Lead Spacing F: 5.0 Pitch of Component P: 12.7	Lead Spacing F: 7.5 Pitch of Component P: 15.0	log log log lead Spacing F: 7.5 Pitch of Component P: 30.0	
DE0705□R221K1K DE0705□R331K1K DE0705□R471K1K DE0805□R681K1K	220 330 470 680	7	5.0				-69	-979	_	_	
DE0905□R102K1K DE1105□R152K1K	1000 1500	9 11		1							
DE1310□R222K1K DE1510□R332K1K DE1710□R472K1K	2200 3300 4700	13 15 17	10.0				-610	_		-477	
DE0707□R221K2K DE0707□R271K2K DE0807□R331K2K	220 270 330	7									
DE0807□R391K2K DE0907□R471K2K DE0907□R561K2K	390 470 560	9		2		-1			-486	_	
DE1007□R681K2K DE1107□R821K2K DE1207□R102K2K	680 820 1000	10 11	7.5				-620				
DE1207□R122K2K DE1207□R152K2K DE1407□R182K2K	1200 1500 1800	12 14				N. O. I.					
DE1507□R222K2K DE1707□R272K2K	2200 2700	15 17			No Code		-810			-477	
DE1910□R332K2K DE2010□R392K2K DE2110□R472K2K	3300 3900 4700	19 20 21	10.0					_	_		
DE0707□R151K3K DE0707□R181K3K DE0707□R221K3K DE0707□R271K3K	150 180 220 270	7									
DE0807□R331K3K DE0907□R391K3K DE1007□R471K3K	330 390 470	9							-486	_	
DE1007□R561K3K DE1107□R681K3K	560 680	10 11	7.5	3.15		_	_				
DE1207□R821K3K DE1307□R102K3K DE1407□R122K3K	820 1000 1200	12 13 14									
DE1507□R152K3K DE1607□R182K3K	1500 1800	15 16							_	-477	
DE1707□R222K3K DE1910□R272K3K	2200 2700	17 19	10.0							_	

C CHARACTERISTIC

*900 pcs. for KH type

						□: Lead Configu	ration/Lead Code	
David Namahan					Straight Long	Straight Short	Double Crimp	Taping* [↑]
Part Number (: optional lead code shown at the right)	Nominal Capacitance (pF)	Max. Body Diameter D (mm)	Lead Spacing F (mm)	Rated Voltage (VDC)		\bigcap		Lead Spacing F: 5.0 Pitch of Component P: 12.7
DE50-6□C331K500 DE50-6□C471K500	330 470	6						
DE50-7 C681K500	680	7						
DE50-8 C102K500	1000	8	5.0	500	No Code	1	-69	-979
DE50-9 C152K500	1500	9		300	No code	-1		
DE51-0□C222K500	2200	10						
DE51-2 C332K500	3300	12						
DE51-4 C472K500	4700	14	10.0				-610	_

†See page 96 for specifications.

*900 pcs. for KH type



LEADED CAPACITORS, NETWORKS & HV CAPACITORS HIGH TEMPERATURE CAPACITORS 250V to 3kVDC E.I.A. CLASS I & II—SPECIFICATIONS & TEST METHODS DE Series

	Item		Specification	Test Method			
2	Operating Tempe Capacitance	rature Range	–25 to +125°C Within the specified tolerance.	The capacitance shall be measured at 20°C with 1 ± 0.2kHz			
			·	(SL: 1 ± 0.2MHz) and 5V(r.m.s.) max.			
3	Q Dissipation Facto	r (D.F.)	$ \begin{array}{ll} C \geq 30 p F\colon Q \geq 1000 & D.F. \leq 0.4\% \; (R \; [250V]) \\ C < 30 p F\colon Q \geq 400 + 20 C^1 (SL) & D.F. \leq 0.2\% \; (R \; [1 \; to \; 3.15 kV]) \\ D.F. \leq 0.3\% \; (C) & D.F. \leq 0.3\% \; (C) \\ \end{array} $	Same condition as capacitance.			
4	Insulation Resistance (I.R.)	Between lead wires	10000M ohms min. (SL, R [1 to 3.15kV], C) (R [250V])	The insulation resistance shall be measured with 500 \pm 50VDC (R [250V]: 100 \pm 15V) within 60 \pm 5 sec. of charging.			
5	Dielectric Strength	Between lead wires	No failure.	The capacitors shall not be damaged when DC voltage of 200% of the rated voltage (in case of rated voltage: 1 to 3.15kV) or DC voltage of 250% of the ratec voltage (in case of rated voltage: 250V, 500V) is applied between the lead wires for 1 to 5 sec. (Charge/discharge current ≤50mA)			
		Body Insulation	No failure.	The capacitor is placed in the container with metal balls of diameter 1mm so that each lead wire, short-circuited, is kept approximately 2mm off the balls as shown in the figure, and AC voltage of 1250V is applied for 1 to 5 sec. between capacitor lead wires and small metals. (Charge/discharge current ≤50mA)			
6	Temperature Cha	racteristic	T.C. Temp. Coefficient Temp. Cap. Change	The capacitance measurement shall be made at each step specified in table.			
			SL +350 to -1000ppm/°C Range C R -25 to within within	Capacitance change from the value of step 3 shall not exceed the limit specified. Char\Step 1 2 3 4 5			
			+85°C ±20% ±15%	SL — 20 ± 2°C 85 ± 2°C/125 ± 2°C 20 ± 2°C			
			+85 to +125°C within ±15%	$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$			
7	Temperature Cycling	Appearance	No marked defect. No marked defect.	The capacitor shall be introduced into the test chamber, and shall be exposed to the temperature conditions of steps 1 to 4 as shown in table at 5 cycles.			
	Cycling	Capacitance Change Q	Within ±5% (SL) Within ±10% (R, C) C ≥ 30pF: Q ≥ 350 D.F. ≤ 0.4% (R, C)	Step Temp. (°C) Time (min.) Step Temp. (°C) Time (min.) 1 -25 ± 3 30 ± 3 3 +125 ± 3 30 ± 3			
		D.F.	$C \le 30pF$: $Q \ge 275 + \frac{5}{2}C^{1}$ (SL)	2 room temp. 3 max. 4 room temp. 3 max.			
		I.R.	1000M ohms min.	Pre-treatment: Capacitor shall be stored at $125 \pm 3^{\circ}$ C for 1 hour, then placed at room condition ² for 24 ± 2 hours before initial measurements (R, C).			
		Dielectric Strength (between lead wires)	Pass the item No. 5.	Post-treatment: Capacitor shall be stored for 1 to 2 hours at room condition ² (SL). Capacitor shall be stored for 24 ± 2 hours at room condition ² (R, C). Measurement Order: I.R., Dielectric Strength → Pre-treatment → Capacitance, D.F. → Temp. Cycling test → Post-treatment → Capacitance, D.F., I.R., Dielectric Strength (R [250V]).			
8	Vibration	Appearance	No marked defect.	The capacitor shall firmly be soldered to the supporting lead wire and vibration			
-	Resistance	Capacitance Change	Within the specified tolerance.	which is 10 to 55Hz in the vibration frequency range, 1.5mm in total amplitude, and about 1 minute in the rate of vibration change from 10Hz to 55Hz and back to 10Hz is applied for a total of 6 hours; 2 hours each in 3 mutually			
		Q. D.F.	$ \begin{array}{ll} C \geq 30 p F\colon Q \geq 1000 & D.F. \leq 0.4\% \; (R \; [250V]) \\ C < 30 p F\colon Q \geq 400 + 20 C^1 (SL) & D.F. \leq 0.2\% \; (R \; [1 \; to \; 3.15kV]) \\ D.F. \leq 0.3\% \; (C) \\ \end{array} $	perpendicular directions.			
9	Soldering Effect	Appearance	No marked defect.	The lead wire shall be immersed into the melted solder of $350 \pm 10^{\circ}$ C up to about 1.5 to 2mm from the main body for 3.5 ± 0.5 sec. and the specified items			
	Litect	Capacitance Change	Within ±2.5% (SL) Within ±10% (R, C)	shall be measured after leaving for 24 ± 2 hours. Pre-treatment: Capacitor shall be stored at $125 \pm 3^{\circ}$ C for 1 hour, then placed at			
		Dielectric Strength (between lead wires)	Pass the item No. 5.	room condition ² for 24 ± 2 hours before initial measurements (R, C). Post-treatment: Capacitor shall be stored for 1 to 2 hours at room condition ² (SL). Capacitor shall be stored for 24 ± 2 hours at room condition ² (R, C). Measurement Order: Dielectric Strength → Pre-treatment → Capacitance → Soldering Effect test → Post-treatment → Capacitance, Dielectric Strength (R [250V]).			
10	Humidity	Appearance	No marked defect.	Set the capacitor for 500^{+24}_{-24} hours at $40 \pm 2^{\circ}$ C in 90 to 95% humidity. Pre-treatment: Capacitor shall be stored at $125 \pm 3^{\circ}$ C for 1 hour, then placed at			
	(Under Steady State)	Capacitance Change	Within ±5% (SL) Within ±10% (R, C)	room condition ² for 24 ± 2 hours before initial measurements (R, C). Post-treatment : Capacitor shall be stored for 1 to 2 hours at room condition ² .			
		Q D.F.	$C \ge 30pF: Q \ge 350$ D.F. $\le 0.4\%$ (R, C) C $< 30pF: Q \ge 275 + \frac{5}{2}C^{1}$ (SL)	Measurement Order: I.R. → Pre-treatment → Capacitance, D.F. → Humidity test → Post-treatment → Capacitance, D.F., I.R. (R [250V]).			
		I.R.	1000M ohms min.	(Charge/discharge current ≤50mA).			
11	Life	Appearance Capacitance Change	No marked defect. Within ±3% (SL) Within ±10% (R, C)	Apply a DC voltage of 200% of the rated voltage (in case of rated voltage: 250V, 500V) or DC voltage of 150% of the rated voltage (in case of rated voltage: 1 to 3.15kV) for 1000 ⁺⁴⁸ ₋₀ hours at 125 ± 2°C. Pre-treatment: Capacitor shall be stored at 125 ± 3°C for 1 hour, then placed at			
		Q D.F.	$C \ge 30pF: Q \ge 350$ D.F. $\le 0.4\%$ (R, C) C $< 30pF: Q \ge 275 + \frac{5}{2}C^{1}$ (SL)	 Pre-treatment: Capacitor shall be stored at 125 ± 3°C for 1 hour, then placed at room condition² for 24 ± 2 hours before initial measurements (R, C). Post-treatment: Capacitor shall be stored for 1 to 2 hours at room condition² 			
		I.R.	2000M ohms min. 1000M ohms min. (SL, R [1 to 3.15kV], C) (R [250V])	(SL). Capacitor shall be stored at 125 ± 3°C for 1 hour, then placed at room condition² for 24 ± 2 hours (R, C). Measurement Order: I.R. → Pre-treatment → Capacitance, D.F. → Life test →			
				I.R. ³ → Post-treatment → Capacitance, D.F. (R [250V]). (Charqe/discharge current ≤50mA).			
12	Strength of Lead	Pull	Lead wire shall not cut off. Capacitor shall not be broken.	As in figure, fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N (1.0kgf) and keep it for 10 ± 1 sec.			
		Bending		Each lead wire shall be subjected to 5N (0.51kgf) weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then a 90° bend in the opposite direction at the rate of one bend in 2 to 3 seconds.			
13	Solderability of L	eads	Lead wire shall be soldered with uniform coating on the axial direction over ³ / ₄ of the circumferential direction.	The lead wire of a capacitor shall be dipped into a methanol solution of 25wt% rosin and then into molten solder of $235 \pm 5^{\circ}$ C for 2 ± 0.5 seconds. In both cases the depth of dipping is up to about 1.5 to 2mm from the root of lead wires			
Ļ			ue (pF). 2"room condition" temperature: 15 to 35°C; humidity: 4				

^{1°}C" expresses nominal capacitance value (pF). 2"room condition" temperature: 15 to 35°C; humidity: 45 to 75%; atmospheric pressure: 860 to 1060hPa. 3The measurement of I.R. will be held in 12 to 24 hours after Life test.