

EMI SUPPRESSION CHOKES

SIMID 01

B 82412

Chip inductors for surface mounting (SMD)

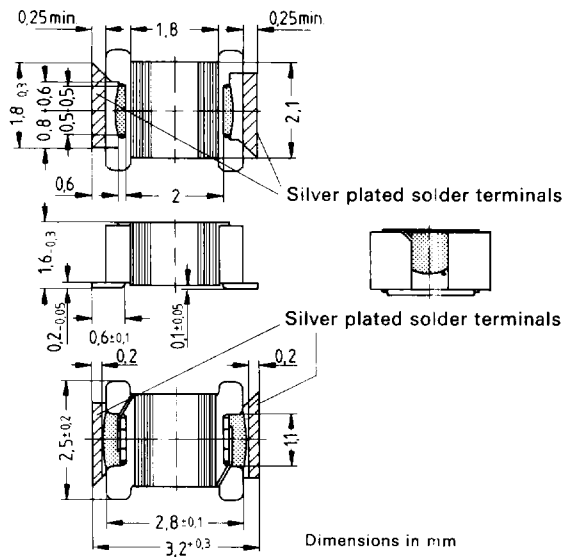
Rated voltage 0.09 to 0.6 A

SIMID 01 series (Siemens Miniature Inductors)

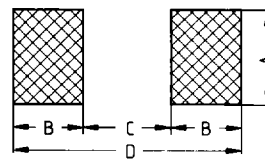
Miniature chip chokes with high-temperature-resistant, single-layer copper winding and a cube-shaped core made of ceramic or ferrite. The winding is lacquer-coated and the wire ends are welded to contact elements (CuSn6) at the face ends of the inductor, with the welded joints being protected by an epoxy resin adhesive. The winding is fixed by epoxy resin.

Chip chokes are intended for automatic placement and all soldering methods.

Due to their special design they are particularly suitable for use in RF circuits such as tuners in car radios, TV sets and video recorders or for application in mobile phones and antenna amplifiers.



Layout recommendation



Dimensions	A	B	C	D
Wave soldering	2.3	1.60	2.1	5.3
Reflow soldering	2.7	1.15	2.1	4.4

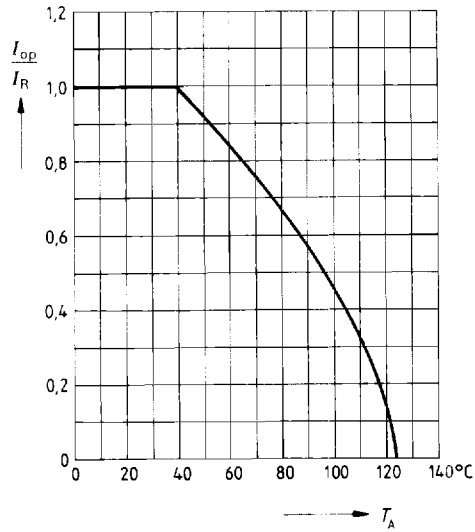
Technical data

Dimensions $l \times w \times h$ (mm) as per EIA	3.2 × 2.5 × 1.6 1210
Rated inductance at measuring frequency	0.022 μ H to 10 μ H 1 MHz
Rated current	referred to 40 °C ambient temperature
DC resistance	measured at 20 °C
Quality	measured with impedance analyzer HP 4194 A
Resonant frequency	measured with scalar network analyzer ZAS by Rhode & Schwarz
DIN climatic category (DIN 40 040)	FKF (−55 °C to +125 °C, humidity category F)
IEC climatic category (IEC 68)	55/125/56

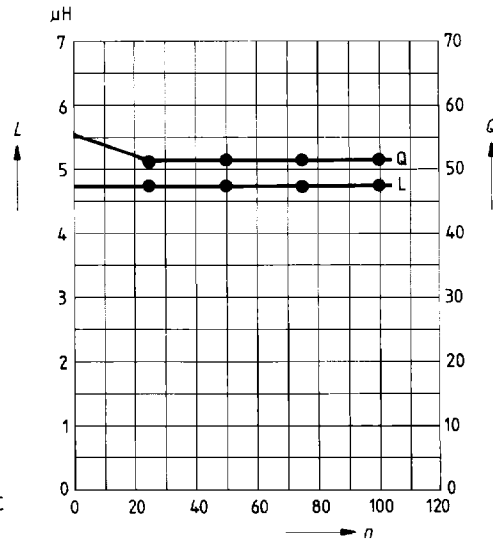
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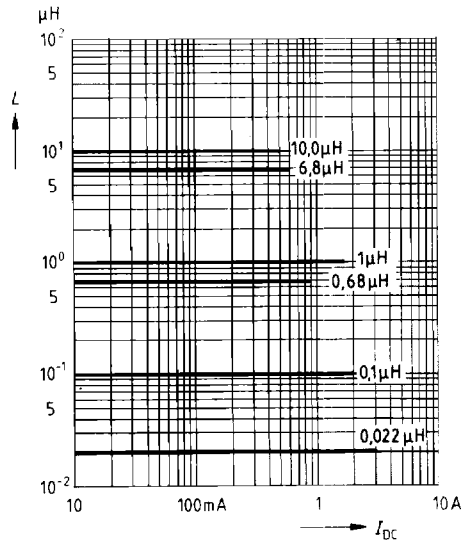
**Current handling capability I_{OP}/I_R
versus ambient temperature T_A**



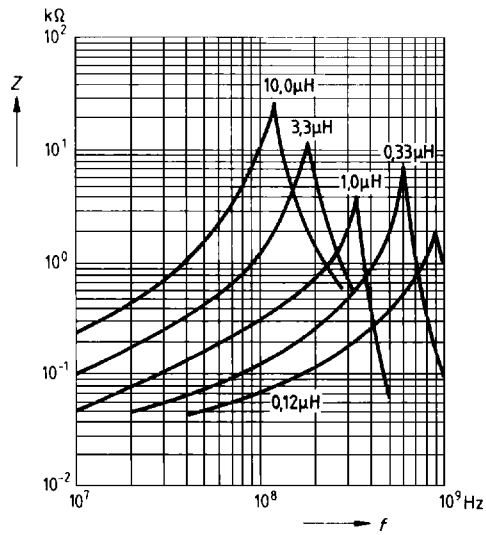
**Inductance L and quality Q
versus number n of dip soldering
procedures (240 °C, 5 s)**



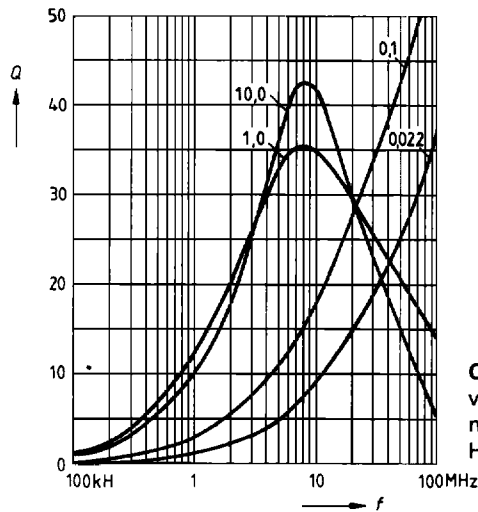
**Inductance L
versus DC load I_{DC} ;
measured with LCR meter HP 4275A**



**Impedance Z
versus frequency f ;
measured with vector analyzer ZPV**



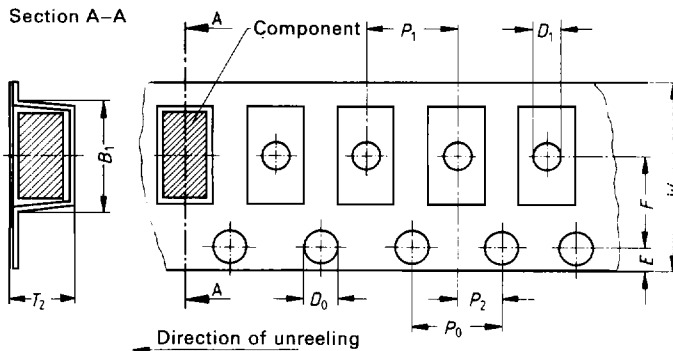
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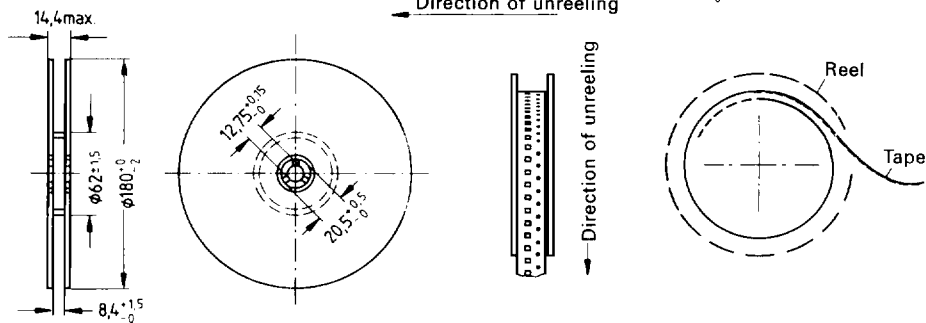
Quality Q
versus frequency f ;
measured with impedance analyzer
HP 4194A

Tape packaging in accordance with IEC 286-3

Dimension	mm
W	8 ± 0.3
P_0	4 ± 0.1
D_0	$1.5 + 0.1$
E	1.75 ± 0.1
F	3.5 ± 0.05
P_2	2 ± 0.05
P_1	4 ± 0.1
D_1	$1.0 + 0.2$
T_2	≤ 2.0
B_1	≤ 4.2



Packing



Marking on blister tape: Inductance and tolerance Δ last 4 digits of ordering code
Marking on packing: Inductance and tolerance in clear text