

MRF428

The RF Line

150 W (PEP) – 30 MHz

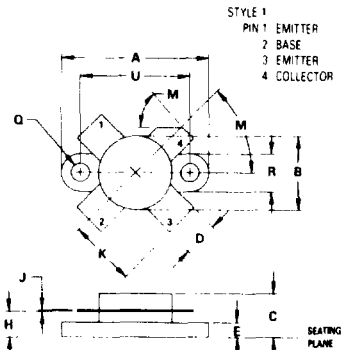
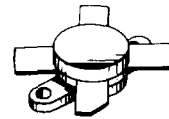
**RF POWER
TRANSISTOR**

NPN SILICON

NPN SILICON RF POWER TRANSISTOR

... designed primarily for high-voltage applications as a high-power linear amplifier from 2.0 to 30 MHz. Ideal for marine and base station equipment.

- Specified 50 Volt, 30 MHz Characteristics –
Output Power = 150 W(PEP)
Minimum Gain = 13 dB
Efficiency = 45%
- Intermodulation Distortion @ 150 W (PEP) –
IMD = -30 dB (Max)
- 100% Tested for Load Mismatch at all Phase Angles with
30:1 VSWR



NOTES
1 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982
2 CONTROLLING DIMENSION INCH

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	24.39	25.14	0.960	0.990
B	11.82	12.95	0.465	0.510
C	5.82	6.96	0.229	0.275
D	5.49	5.96	0.216	0.235
E	2.14	2.79	0.084	0.110
H	3.66	4.52	0.144	0.178
J	0.08	0.17	0.003	0.007
K	11.95	—	0.435	—
M	45	NOM	45	NOM
Q	2.93	3.30	0.115	0.130
R	6.25	6.47	0.246	0.255
U	18.29	18.54	0.720	0.730

CASE 211-11

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	55	Vdc
Collector-Base Voltage	V _{CBO}	110	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current – Continuous	I _C	20	Adc
Withstand Current – 10 s	—	30	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	320 1.83	Watts W/°C
Storage Temperature Range	T _{stg}	- 65 to + 150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC}	0.5	°C/W

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage (I _C = 200 mA dc, I _B = 0)	V _{(BR)CEO}	55	—	—	Vdc
Collector-Emitter Breakdown Voltage (I _C = 100 mA dc, V _{BE} = 0)	V _{(BR)CES}	110	—	—	Vdc
Collector-Base Breakdown Voltage (I _C = 100 mA dc, I _E = 0)	V _{(BR)CBO}	110	—	—	Vdc
Emitter-Base Breakdown Voltage (I _E = 10 mA dc, I _C = 0)	V _{(BR)EBO}	4.0	—	—	Vdc
ON CHARACTERISTICS					
DC Current Gain (I _C = 5.0 A dc, V _{CE} = 5.0 Vdc)	h _{FE}	10	30	—	—
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 50 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	—	200	250	pF
FUNCTIONAL TESTS					
Common-Emitter Amplifier Gain (V _{CC} = 50 Vdc, P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A dc, f = 30 MHz)	G _{PE}	13	15	—	dB
Output Power (V _{CE} = 50 Vdc, f = 30 MHz)	P _{out}	150	—	—	W PEP
Collector Efficiency (V _{CC} = 50 Vdc, P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A dc, f = 30 MHz)	η	45	—	—	%
Intermodulation Distortion (1) (V _{CE} = 50 Vdc, P _{out} = 150 W (PEP), I _C = 3.32 A dc)	IMD	—	-33	-30	dB
Electrical Ruggedness (V _{CC} = 50 Vdc, P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A dc, f = 30 MHz) VSWR 30:1 at all Phase Angles	No Degradation in Output Power				

(1) To Mil Std 1311 Version A, Test Method 2204B, Two Tone, Reference Each Tone.

FIGURE 1 -- 30 MHz TEST CIRCUIT SCHEMATIC

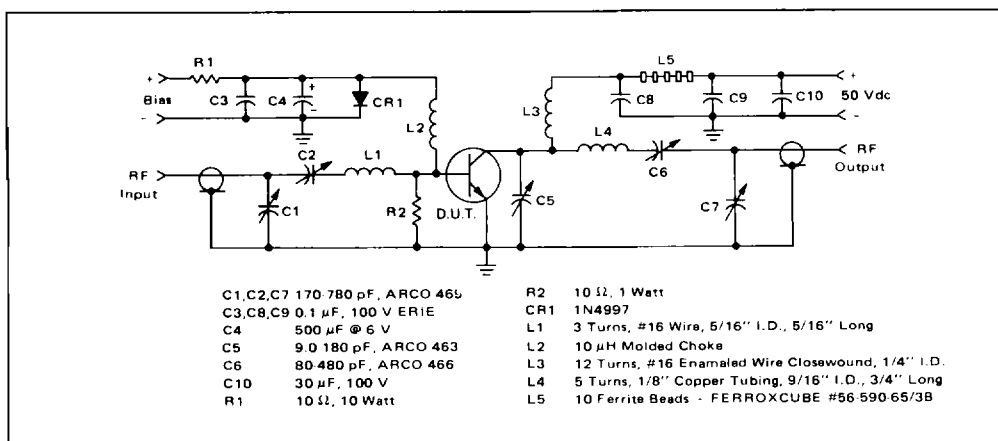


FIGURE 2 – OUTPUT POWER versus INPUT POWER

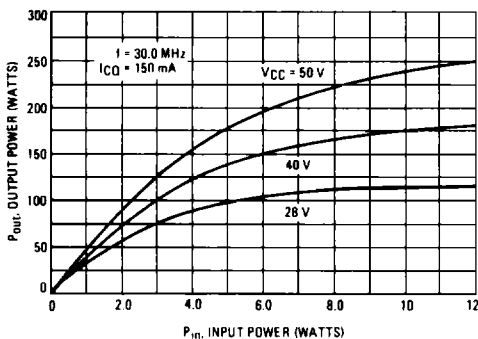


FIGURE 3 – OUTPUT POWER versus SUPPLY VOLTAGE

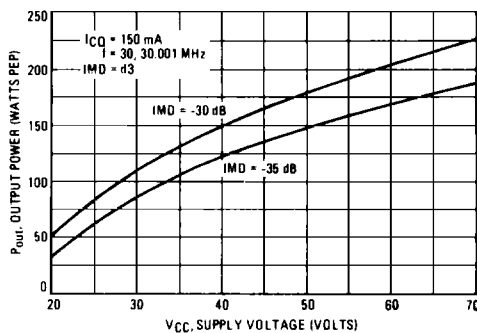


FIGURE 4 – POWER GAIN versus FREQUENCY

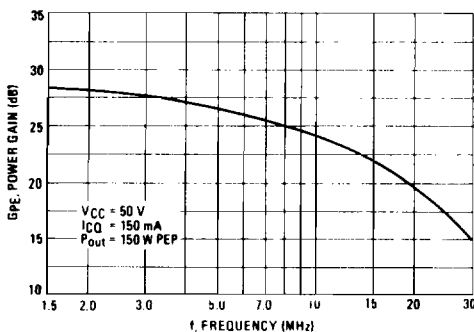
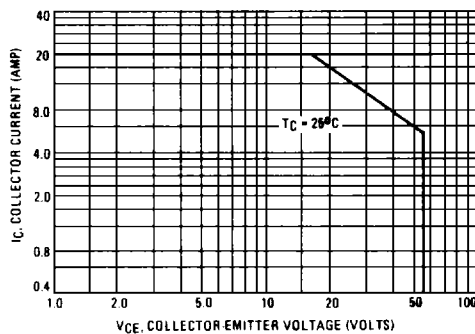


FIGURE 5 – DC SAFE OPERATING AREA



INTERMODULATION DISTORTION versus OUTPUT POWER

FIGURE 6 – $V_{CC} = 40\text{ Vdc}$

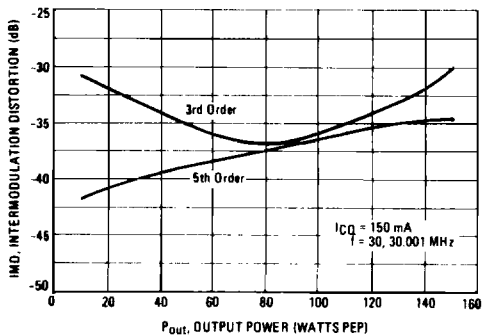
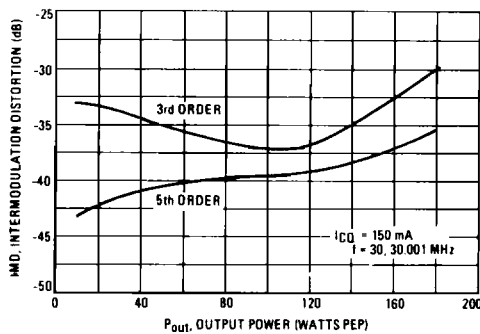


FIGURE 7 – $V_{CC} = 50\text{ Vdc}$



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FIGURE 8 – OUTPUT CAPACITANCE versus FREQUENCY

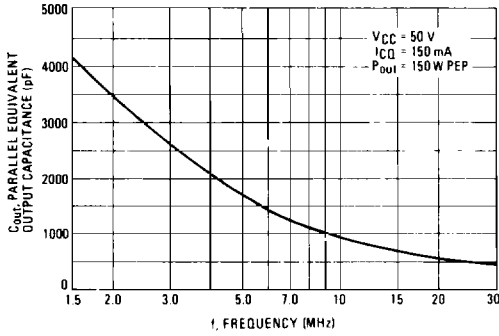


FIGURE 9 – OUTPUT RESISTANCE versus FREQUENCY

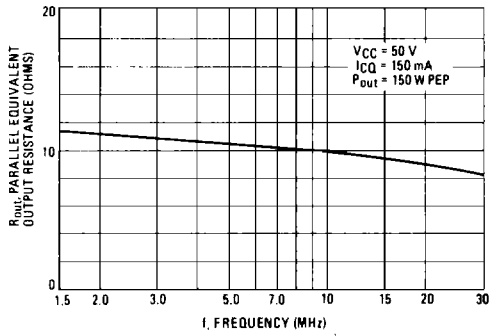


FIGURE 10 – SERIES EQUIVALENT IMPEDANCE

